



AREA SELECTION FORM

We cannot process your application without this form

Family Surname: _____ First Name: _____

Date of Birth: _____ E-mail Address: _____

1. **TEST SCORES:** All applicants are required to take the GRE General Test and are strongly encouraged to take a Subject Test, if a subject test is available in the applicant's undergraduate major. Please list your test scores below. *Official test results from ETS must also be submitted.*

A. GRE General Test score:

Quantitative _____ Analytic _____ Verbal _____ Total _____

GRE Subject Test score:

Computer Science _____ Mathematics _____ Electrical Engineering _____ Other – (specify subject) _____

B. TOEFL score (if applicable): _____ TSE score (if applicable): _____

2. **AREA SELECTION FORM:** All applicants must indicate at least one subfield and at the most two subfields listed below. If you choose to mark two areas, mark your first choice “1” and your second choice with “2.” Check one to three faculty members in your selected sub-area with whom you wish to pursue your research. You are urged to match your interests with the faculty of Computer Science and Engineering (CSE) at the University at Buffalo and choose areas accordingly. Further information on faculty research interests is detailed at <http://www.cse.buffalo.edu>.

<input type="checkbox"/> Algorithms and Theory <input type="checkbox"/> Beal, M. <input type="checkbox"/> He, X. <input type="checkbox"/> Miller, R. <input type="checkbox"/> Ngo, H. <input type="checkbox"/> Regan, K. <input type="checkbox"/> Selman, A. <input type="checkbox"/> Xu, J. <input type="checkbox"/> Zhong, S.	<input type="checkbox"/> Augmentative Technology for the Handicapped <input type="checkbox"/> Buckley, M. (MS only) <input type="checkbox"/> Schindler, K. (MS only)	<input type="checkbox"/> Computer Networks and Distributed Systems <input type="checkbox"/> Chaudhary, V. <input type="checkbox"/> Demirbas, M. <input type="checkbox"/> Ngo, H. <input type="checkbox"/> Qiao, C. <input type="checkbox"/> Sridhar, R. <input type="checkbox"/> Upadhyaya, S. <input type="checkbox"/> Xu, J. <input type="checkbox"/> Zhong, S.	<input type="checkbox"/> Computer Science Education <input type="checkbox"/> Alphonse, C. (MS only) <input type="checkbox"/> Buckley, M. (MS only) <input type="checkbox"/> Ramamurthy, B.	<input type="checkbox"/> Computer Security and Information Assurance <input type="checkbox"/> Govindaraju, V. <input type="checkbox"/> Sridhar, R. <input type="checkbox"/> Upadhyaya, S. <input type="checkbox"/> Zhong, S.	<input type="checkbox"/> Computer Vision, Pattern Recognition, Data Mining, and Machine Learning <input type="checkbox"/> Beal, M. <input type="checkbox"/> Govindaraju, V. <input type="checkbox"/> Lorigo, L. <input type="checkbox"/> Scott, P. <input type="checkbox"/> Srihari, R. <input type="checkbox"/> Srihari, S. <input type="checkbox"/> Zhang, A.
<input type="checkbox"/> Database and Multimedia Systems <input type="checkbox"/> Chomicki, J. <input type="checkbox"/> Petropoulos, M. <input type="checkbox"/> Srihari, R. <input type="checkbox"/> Zhang, A. <input type="checkbox"/> Zhong, S.	<input type="checkbox"/> High Performance & Grid Computing, Cyberinfrastructure, and Computational Science <input type="checkbox"/> Chaudhary, V. <input type="checkbox"/> Miller, R. <input type="checkbox"/> Ramamurthy, B. <input type="checkbox"/> Zhang, A.	<input type="checkbox"/> Knowledge Representation, Computational Linguistics, and Cognitive Science <input type="checkbox"/> Alphonse, C. (MS only) <input type="checkbox"/> Chomicki, J. <input type="checkbox"/> Rapaport, W. <input type="checkbox"/> Shapiro, S. <input type="checkbox"/> Srihari, R.	<input type="checkbox"/> Medical Applications and Bioinformatics <input type="checkbox"/> Beal, M. <input type="checkbox"/> Chaudhary, V. <input type="checkbox"/> Fischer, D. <input type="checkbox"/> Govindaraju, V. <input type="checkbox"/> Lorigo, L. <input type="checkbox"/> Xu, J.	<input type="checkbox"/> Software Systems and Languages <input type="checkbox"/> Chomicki, J. <input type="checkbox"/> Demirbas, M. <input type="checkbox"/> Jayaraman, B. <input type="checkbox"/> Petropoulos, M. <input type="checkbox"/> Zhang, A.	<input type="checkbox"/> VLSI, Computer Architecture, and Embedded Systems <input type="checkbox"/> Chaudhary, V. <input type="checkbox"/> Sridhar, R. <input type="checkbox"/> Upadhyaya, S.