

Consequences of the Universal RAM Simulator TI

① For every program P written in \underline{U} (known) HI we can build a DTM M_P s.t. for all x on Stdin

$M_P(x)$ has an emulating computation of $P(x)$.

Proof: ② Compile P to object code O_P in "my

③ Design M_P with an "initial bank" of states that

- First copy x as $[x]$ on Tape 3 of U .

- Use $|O_P|$ special one-off states to overwrite x on tape 1 with the textual code of O_P 's instruction

Now we have the initial setup for U running O_P on x so go to the start state of U . $M_P = \boxed{\text{Init}} \xrightarrow{e} \boxed{U}$

$M_P(x) = U(P, x) = P(x)$. Size $\propto |O_P| + |U| \approx |P| + c$

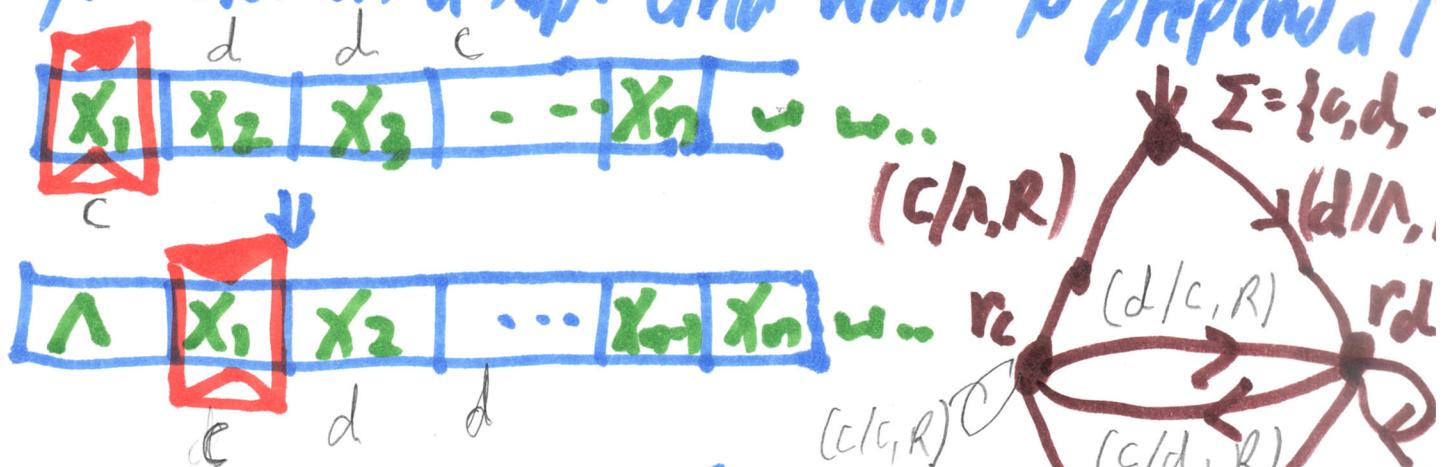
- The time used by a computation is its # of steps

- * The space used is the # of cells in which a char was changed to a different char.

(2) MB's Theorem [Steve Cook, 1971] Not his famous 1971 Thm on SAT!

There is a clever way to build V so that every t steps of $P(x)$ judged at "fair cost" is simulated by $O(t^2)$ steps of V . If you use an m -bit int you get charged in time $U(n)$

Ideas Involved. Suppose we get input $X = X_1, X_2, \dots$ left-justified on a tape and want to prepend a /



Furthermore, this "Shift Over" routine can be called to make more room in the middle of a tape, e.g. to write to all $e \neq \lambda$ in a "register" of V .

[label # val]

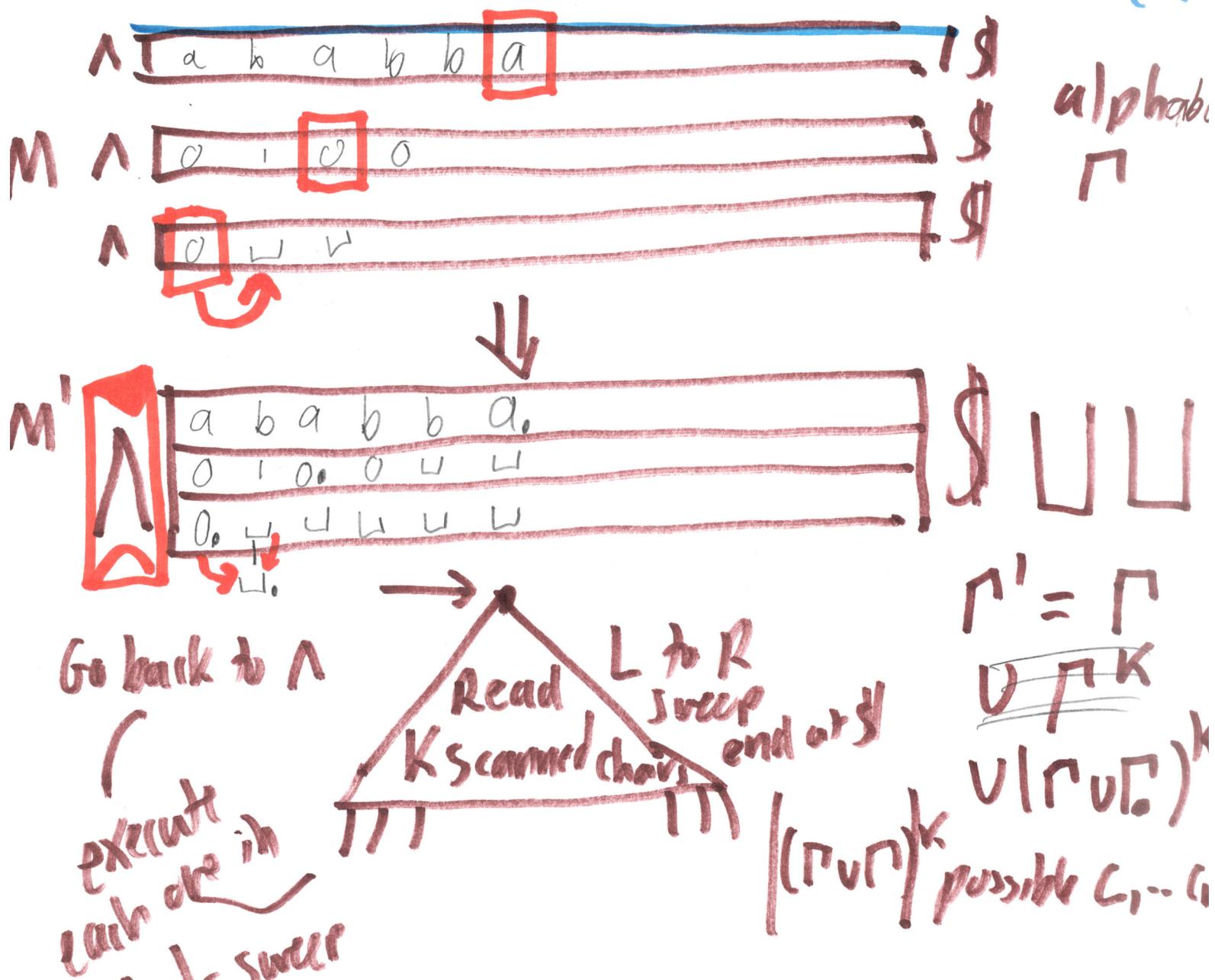
Doing this simplistically gives time $\approx O(t^4)$

A clever doubling scheme like C++ vector gives the $O(t^2)$.

We can attach a separate copy of "sh. over", entered on ']' at any state q of V . Can picture it like a Unix "daemon".

daemons

③ A TM with 3 (or any number K) tapes
^{Time}
 can be simulated by a one-tape TM in time $O(t^2)$ (3)



Added: The notes mention that Sipser's text gives a different method that manages K regions on one tape. [like my "registers" but separated by # not []]. That uses a similar "shift over" routine and (hence) is less efficient as well as "messy".