Call for Papers

International Workshop on
Embedded Systems and Communications Security (ESCS 2009)

Held in conjunction with SRDS 2009, Niagara Falls, New York, U.S.A., September 27, 2009
http://www.cse.buffalo.edu/srds2009/ESCS.html

The strong growth in mobile devices, networked embedded systems, wireless sensors, in/inter-vehicular computing applications, the ever increasing information computing power offered by computing clusters and information services over networks (stationary and mobile) presents new challenges, especially with respect to the security of integrated applications and environments. Security solutions need to address cost, resource constraints, enterprise/embedded interconnectivity, real time deadlines, multi-vendor integration issues, potentially unreliable mobile systems with low-bandwidth communication, and physical constraints often found in embedded applications.

We solicit previously unpublished papers offering novel research contributions in these aspects of computer security that focus on network computing, embedded and mobile/ubiquitous systems. Papers may present advances in the theory, design, implementation, analysis, or empirical evaluation of secure systems, infrastructure, architectures, algorithms and protocols.

Topics include, but are not limited to:

- Embedded System and hardware Security/Trust
- Intrusion detection and prevention
- Security for transport systems (cars, rail, aerospace, etc.)
- Security in safety-critical systems
- Methods for Key Management and Access Control.
- Robust Secure Protocols and Cryptographic Methods
- Embedded control network authentication and integrity
- Secure embedded device software updates
- Cryptanalysis Methods
- Power/Timing based attacks and mitigation
- Design for security
- Security of Cyberphysical Systems (enterprise+embedded composites)
- License management for disconnected embedded systems
- Tamper resistance

WORKSHOP COMMITTEE:

- **Chair:** Vinodh Gopal, Intel Corporation, USA
- Dr. Michael Kurdziel, Harris Corp, USA
- Dr. Miroslav Velev, Aries Design Automation, USA
- Prof. Ramalingam Sridhar, SUNY-Buffalo, USA
- Prof. Philip Koopman, Carnegie-Mellon University, USA
- Prof. Berk Sunar, Worcester Polytechnic Institute, USA
- Prof. Cetin Kaya Koc, University of California, Santa Barbara, USA
- Dr. Divya Arora, Intel Corporation, USA
- Dr. Nachiketh Potlapally, Intel Corporation, USA

SUBMISSION GUIDELINES:

Authors should prepare a Portable Document Format (PDF) version of their full paper. Regular papers must be no longer than 10 pages, papers on evolving research and work in progress should be no longer than 5 pages, following the IEEE two-column format for conference proceedings. The font size must be no smaller than 10 points, and must fit properly on US "Letter"-sized paper (8.5 x 11 inches). All the submissions should be made through EasyChair (http://www.easychair.org/conferences/?conf=escs2009).

PUBLICATION:

All submitted papers will be peer-reviewed and the accepted papers will be included in a Workshop Proceedings distributed at the Symposium. The papers will also be made available online from the workshop webpage: http://www.cse.buffalo.edu/srds2009/ESCS.html.

IMPORTANT DATES:

- Submission deadline: **July 6, 2009**
- Author notification: **August 10, 2009**
- Camera-ready: **August 31, 2009**
- Workshop: **September 27, 2009**

For more details, contact: Mr. Vinodh Gopal at vinodh.gopal@intel.com. **Visit:** http://www.cse.buffalo.edu/srds2009