

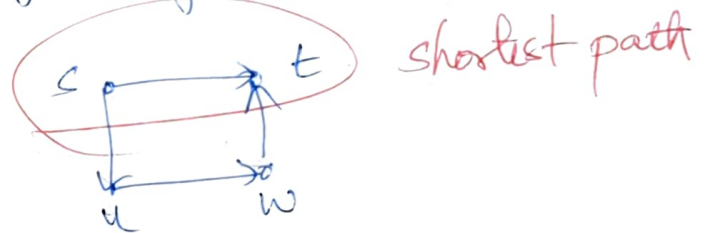
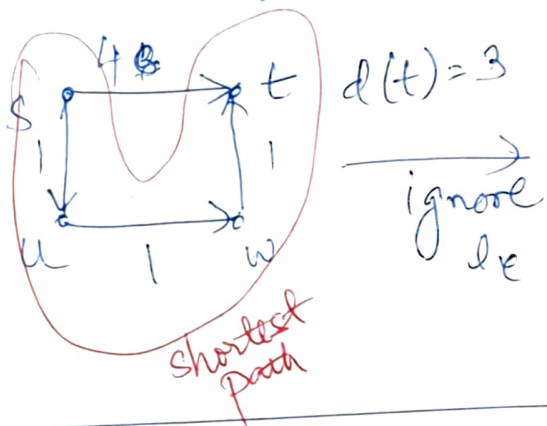
Oct 13

Special case: $l_e = L \quad \forall e \in E$

↳ Use HW3 Q3 algo (basically ignore l_e)
↳ multiply $d(t)$ from \downarrow by L

Algo idea: Reduce the general case ($l_e \geq 0 \quad \forall e \in E$)
to the case of $l_e = 1$.

Idea 1: Ignore all the edge lengths



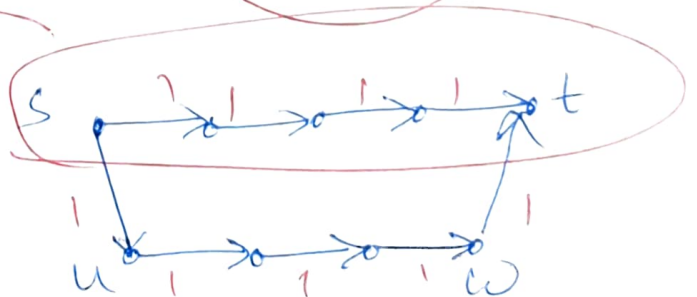
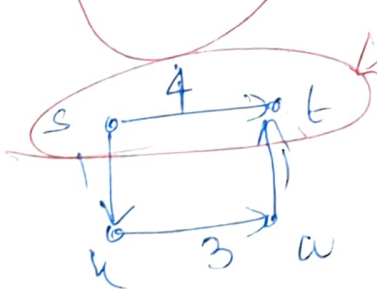
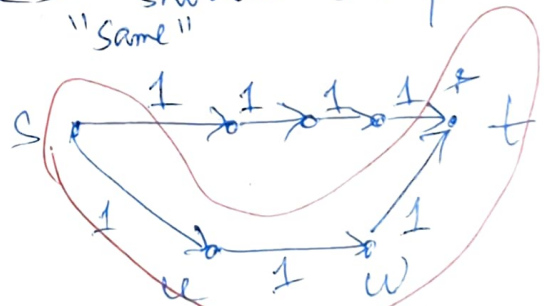
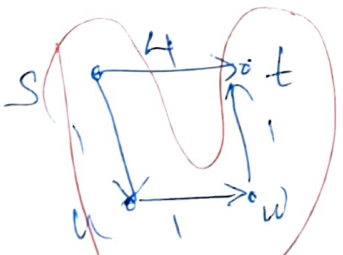
Algo idea:

$$G = (V, E) \rightarrow G' = (V', E')$$

$$\{l_e\}_{e \in E} \quad \{l'_{e'}\}_{e' \in E}$$

Luks's reduction

shortest $s-t$ path \leftrightarrow shortest $s-t$ path
"same"



$G \rightarrow G'$ (1) replace each edge $e \in E$ by
 a path of length l_e in G'
 (new paths do not share any edges)

\rightarrow Run HW3 Q3 algo on G'
 \hookrightarrow If P' is the shortest $s-t$ path in G'
 \hookrightarrow return the corresponding path P in G

$G \rightarrow G' = (V', E')$

runtime of HW3 Q3 algo = $O(|V'| + |E'|)$
 $n' = |V'|, m' = |E'|$

Ex! Convert $G \rightarrow G'$ in $O(n' + m')$ time
 Convert $P' \rightarrow P$ in $O(n' + m')$ time

OVERALL: $O(n' + m')$

Q: How does this relate to $O(n+m)$?

$n' \leq l_{\max} \cdot n$

$l_{\max} = \max_{e \in E} l_e$

$m' \leq l_{\max} \cdot m$

$\Rightarrow O(l_{\max} \cdot n + l_{\max} \cdot m) = O(l_{\max} (n+m))$

If $l_{\max} = O(1) \Rightarrow \checkmark$

If $l_{\max} = n^{100} \Rightarrow O(n^{100} (n+m))$

$O(1)$ register as long as $l_{\max} = n$
 $O(1)$

Aside RAM model unit of space is a register

If you have n items in input, each register $O(\log n)$ bits
 \Rightarrow Adjacency list $O(n+m)$ registers

Q: How many registers would you need to represent $\leq n^{100}$

Q: How many bits

$\Rightarrow \approx 100$ registers $\equiv O(1)$ registers.

$\leq n^{100}$
 $\approx 100 \log n$
 $\approx O(\log n)$