Greedy Algorithm for Interval Scheduling

$\mathsf{Schedule}(s, f, n)$

1:
$$A \leftarrow \{1, 2, \cdots, n\}, S \leftarrow \emptyset$$

2: while $A \neq \emptyset$ do

3:
$$j \leftarrow \arg \min_{j' \in A} f_{j'}$$

4:
$$S \leftarrow S \cup \{j\}; A \leftarrow \{j' \in A : s_{j'} \ge f_j\}$$

5: return S

Running time of algorithm?

• Naive implementation: $O(n^2)$ time

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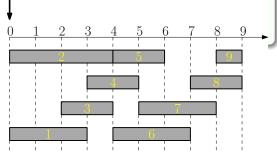
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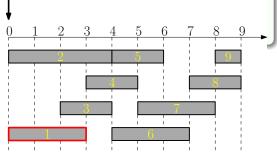
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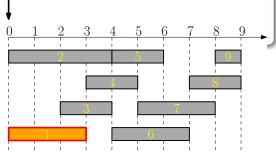
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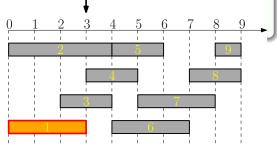


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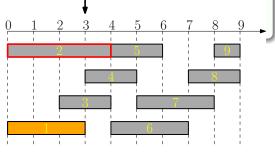


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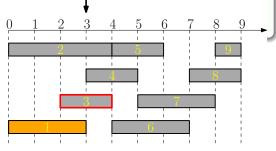


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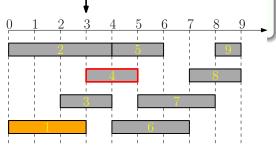


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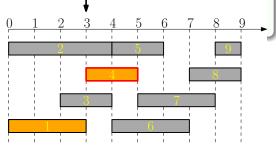


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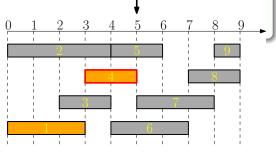


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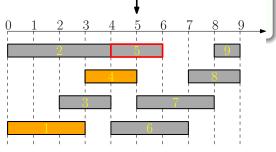


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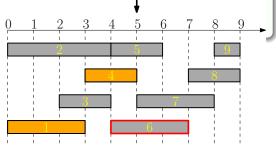


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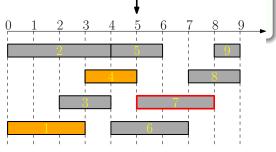


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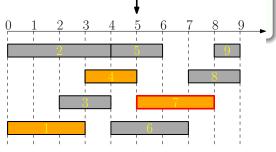


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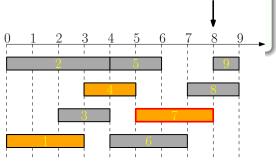


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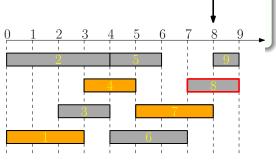


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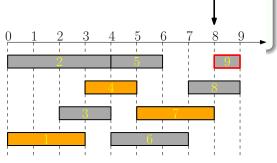


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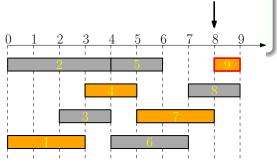


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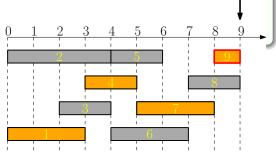


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Outline

Toy Example: Box Packing

Interval SchedulingInterval Partitioning

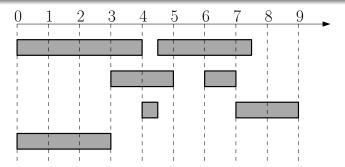
3 Offline Caching

- Heap: Concrete Data Structure for Priority Queue
- 4 Data Compression and Huffman Code

5 Summary

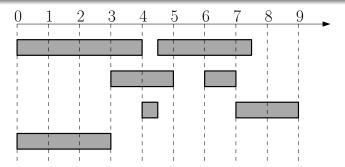
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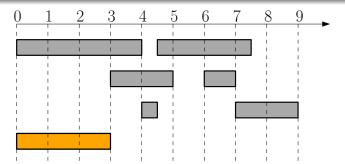
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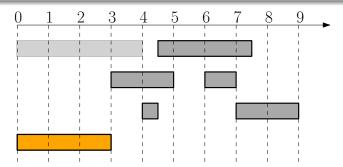
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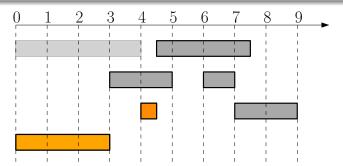
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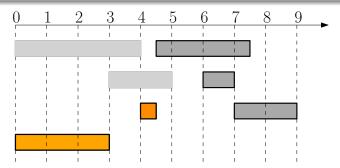
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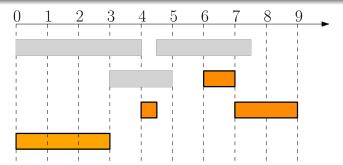
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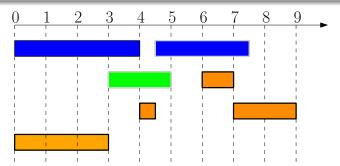
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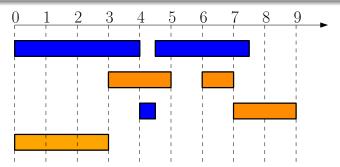
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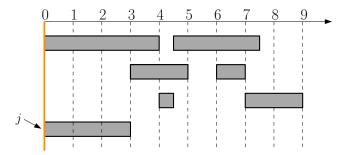
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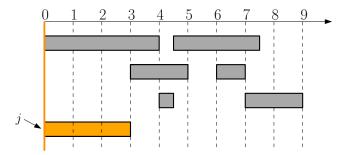
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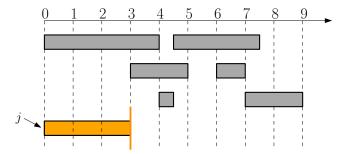
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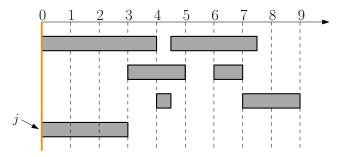
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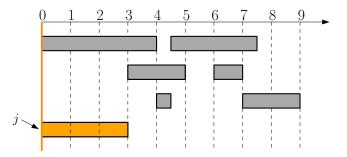


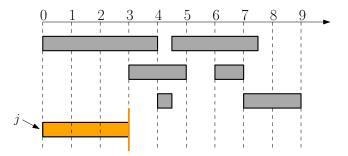
$\mathsf{Partition}(s, f, n)$

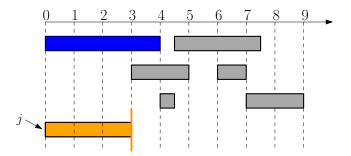
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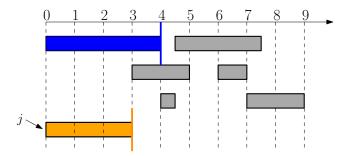
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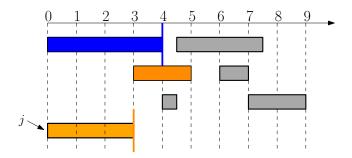


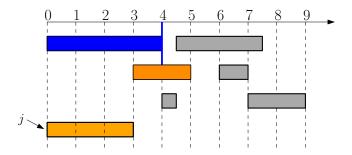


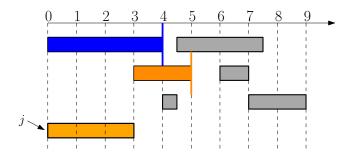


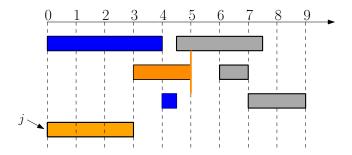


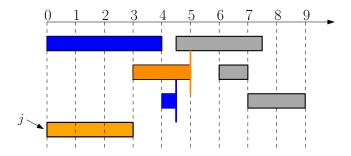


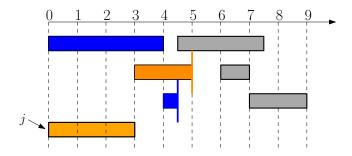


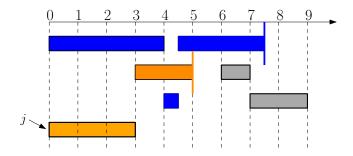


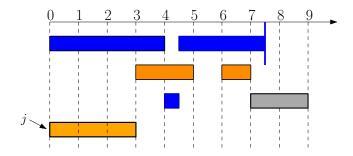


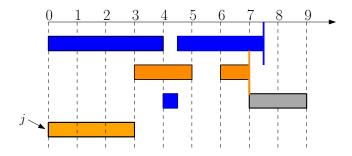


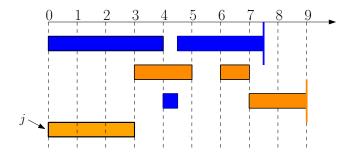












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Obs. Greedy algorithm never schedules two incompatible jobs in the same machine.

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- By the Observation in the previous slide, an optimal solution $\geq d$. Thus the greedy algorithm is optimal.

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- Naive implementation: ${\cal O}(n^2)$ time
- Clever implementation: $O(n \lg n)$ time with Priority Queue.

Outline

Toy Example: Box Packing

Interval Scheduling
Interval Partitioning

3 Offline Caching

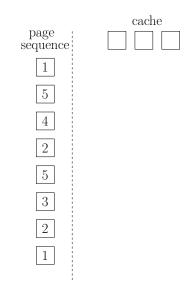
• Heap: Concrete Data Structure for Priority Queue

4 Data Compression and Huffman Code

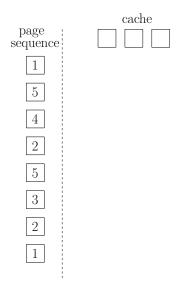
5 Summary

- Cache that can store \boldsymbol{k} pages
- Sequence of page requests

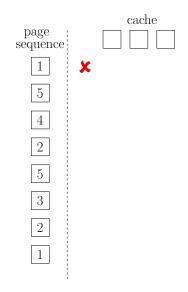
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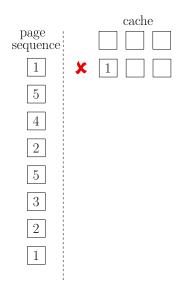
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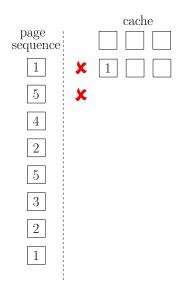
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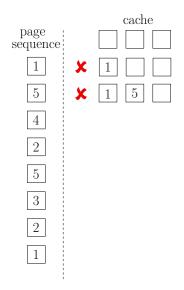
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		cache		
page sequence				
1	X	1		
5	x	1	5	
4	X			
2				
5				
3				
2				
1				

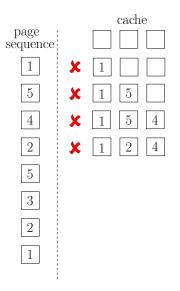
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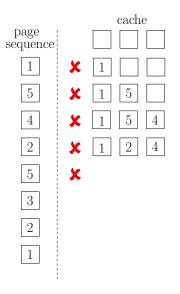
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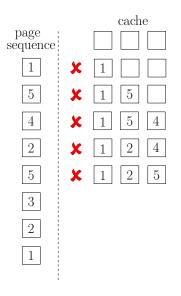
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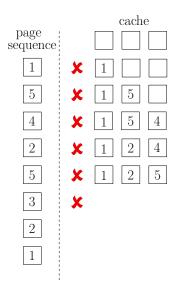
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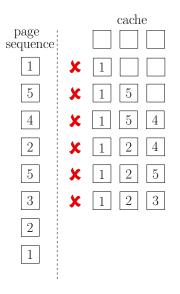
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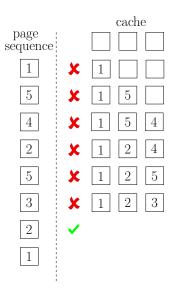
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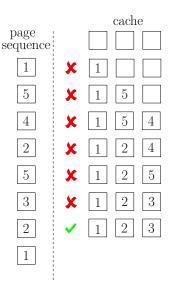
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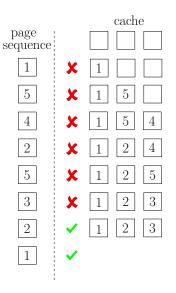
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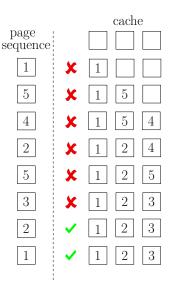
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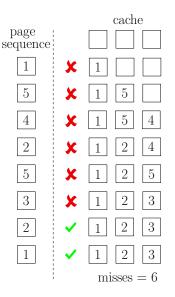
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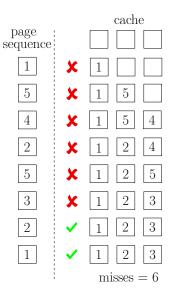


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- Cache hit happens if requested page already in cache.
- Goal: minimize the number of cache misses.



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