Tree-based methods on GPUs

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School of Mathematical Sciences, Monash University Clayton, VIC Mar 3, 2010



Outline

- Introduction
- Short Introduction to FMN
- Serial Implementation
- Parallel FMM
- Multicore FMM

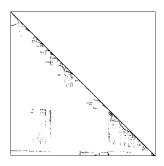


Scientific Computing Challenge

How do we create reusable implementations which are also efficient?

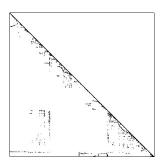
Scientific Computing Insight

Structures are conserved, but tradeoffs change.



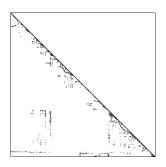
- Sparse matrix-vector product has a common structure
- Different storage formats are chosen based upon
 - architecture
 - PDE





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$$A x = b$$
{ b, Ab, A(Ab), A(A(Ab)), ...}

This is how PETSc works:

- Krylov solvers have a common structure
- Different solvers are chosen based upon
 - problem characteristics
 - architecture



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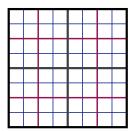
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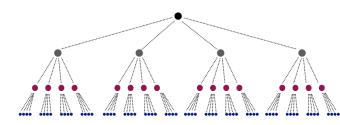


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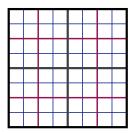


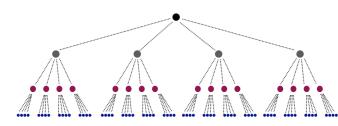


This is how treecodes work:

- Hierarchical algorithms have a common structure
- Different analytical and geometric decisions depend upon
 - problem configuration
 - accuray requirements



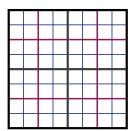


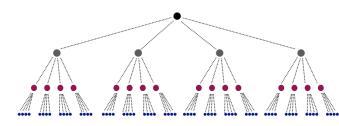


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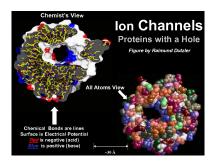




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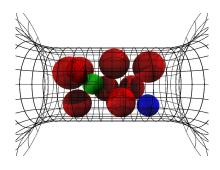
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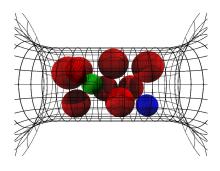
This is how biology works:

- For ion channels, Nature uses the same
 - protein building blocks
 - energetic balances
- Different energy terms predominate for different uses



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Divide the work into levels:

- Model
- Algorithm
- Implementation

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Spiral Project:

- Discrete Fourier Transform (DSP)
- Fast Fourier Transform (SPL)
- C Implementation (SPL Compiler)

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Divide the work into levels:

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FLAME Project:

- Abstract LA (PME/Invariants)
- Basic LA (FLAME/FLASH)
- Scheduling (SuperMatrix)

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FEniCS Project:

- Navier-Stokes (FFC)
- Finite Element (FIAT)
- Integration/Assembly (FErari)

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Treecodes:

- Kernels with decay (Coulomb)
- Treecodes (PetFMM)
- Scheduling (PetFMM-GPU)

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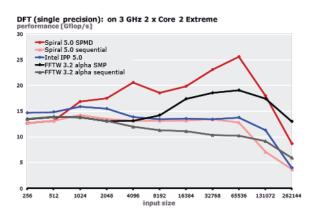
Treecodes:

- Kernels with decay (Coulomb)
- Treecodes (PetFMM)
- Scheduling (PetFMM-GPU)

Each level demands a strong abstraction layer

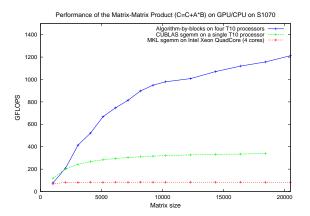
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Spiral



- Spiral Team, http://www.spiral.net
- Uses an intermediate language, SPL, and then generates C
- Works by circumscribing the algorithmic domain

FLAME & FLASH



- Robert van de Geijn, http://www.cs.utexas.edu/users/flame
- FLAME is an Algorithm-By-Blocks interface
- FLASH/SuperMatrix is a runtime system

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- Short Introduction to FMM
 - Spatial Decomposition
 - Data Decomposition
- Serial Implementation
- Parallel FMM
- Multicore FMM

FMM Applications

FMM can accelerate both integral and boundary element methods for:

- Laplace
- Stokes
- Elasticity

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Advantages

- Mesh-free
- O(N) time
- Distributed and multicore (GPU) parallelism
- Small memory bandwidth requirement

Fast Multipole Method

FMM accelerates the calculation of the function:

$$\Phi(x_i) = \sum_j K(x_i, x_j) q(x_j) \tag{1}$$

- Accelerates $\mathcal{O}(N^2)$ to $\mathcal{O}(N)$ time
- The kernel $K(x_i, x_i)$ must decay quickly from (x_i, x_i)
 - Can be singular on the diagonal (Calderón-Zygmund operator)
- Discovered by Leslie Greengard and Vladimir Rohklin in 1987
- Very similar to recent wavelet techniques

Fast Multipole Method

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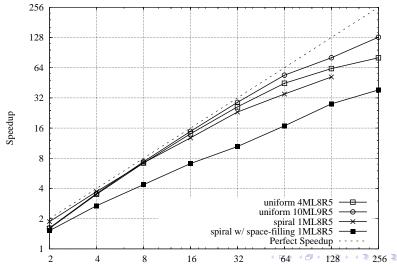


PetFMM

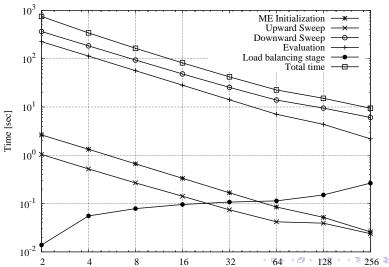
PetFMM is an freely available implementation of the Fast Multipole Method http://barbagroup.bu.edu/Barba group/PetFMM.html

- Leverages PETSc
 - Same open source license
 - Uses Sieve for parallelism
- Extensible design in C++
 - Templated over the kernel
 - Templated over traversal for evaluation
- MPI implementation
 - Novel parallel strategy for anisotropic/sparse particle distributions
 - PetFMM—A dynamically load-balancing parallel fast multipole library
 - 86% efficient strong scaling on 64 procs
- Example application using the Vortex Method for fluids
- (coming soon) GPU implementation

PetFMM CPU Performance Strong Scaling



PetFMM CPU Performance Strong Scaling



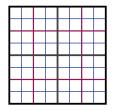
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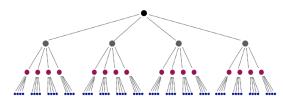
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- Spatial Decomposition
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Spatial Decomposition

Pairs of boxes are divided into near and far:

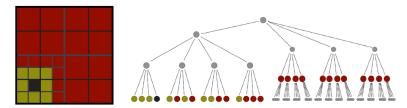




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Spatial Decomposition

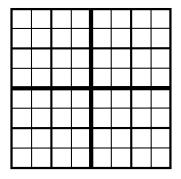
Pairs of boxes are divided into near and far:



Neighbors are treated as very near.

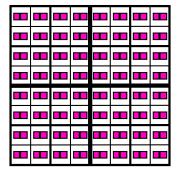
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FMM in Sieve



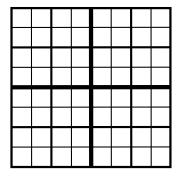
- The Quadtree is a Sieve
 - with optimized operations
- Multipoles are stored in Sections
- Two Overlaps are definedNeighbors
- Completion moves data for
 - NeighborsInteraction List

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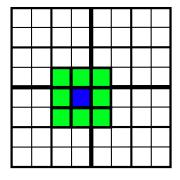


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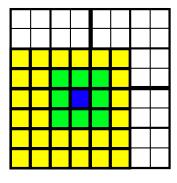
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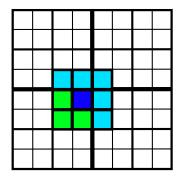
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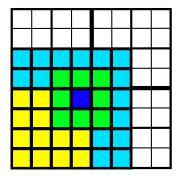
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 - Data Decomposition



FMM requires data over the Quadtree distributed by:

- box
 - Box centers, Neighbors
- box + neighbors
 - Blobs
- box + interaction list
 - Interaction list cells and values
 - Multipole and local coefficients

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Notice this is multiscale since data is divided at each level

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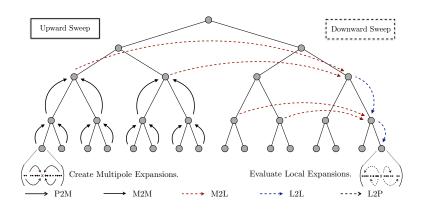
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- Serial Implementation
 - Control Flow
 - Interface
- Parallel FMM
- Multicore FMM

Outline

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 - Interface



FMM Control Flow

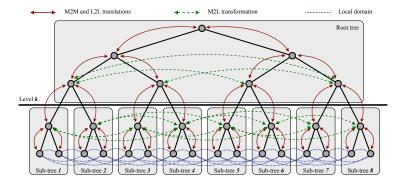


Kernel operations will map to GPU tasks.



FMM Control Flow

Parallel Operation



Kernel operations will map to GPU tasks.



Outline

- Serial Implementation
 - Control Flow
 - Interface



Evaluator Interface

- initializeExpansions(tree, blobInfo)
 - Generate multipole expansions on the lowest level
 - Requires loop over cells
 - O(p)
- upwardSweep(tree)
 - Translate multipole expansions to intermediate levels
 - Requires loop over cells and children (support)
 - O(p²)
- downwardSweep(tree)
 - Convert multipole to local expansions and translate local expansions on intermediate levels
 - Requires loop over cells and parent (cone)
 - $O(p^2)$



Evaluator Interface

- evaluateBlobs(tree, blobInfo)
 - Evaluate direct and local field interactions on lowest level
 - Requires loop over cells and neighbors (in section)
 - $O(p^2)$
- evaluate(tree, blobs, blobInfo)
 - Calculate the complete interaction (multipole + direct)

Kernel Interface

Method	Description
P2M(t)	Multipole expansion coefficients
L2P(t)	Local expansion coefficients
M2M(t)	Multipole-to-multipole translation
M2L(t)	Multipole-to-local translation
L2L(t)	Local-to-local translation
evaluate(blobs)	Direct interaction

- Evaluator is templated over Kernel
- There are alternative kernel-independent methods
 - kifmm3d



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- Divide tree into a root and local trees
- Distribute local trees among processes
- Provide communication pattern for local sections (overlap)
 - Both neighbor and interaction list overlaps
 - Sieve generates MPI from high level description

How should we distribute trees?

- Multiple local trees per process allows good load balance
- Partition weighted graph
 - Minimize load imbalance and communication
 - Computation estimate:

Leaf
$$N_i p$$
 (P2M) + $n_i p^2$ (M2L) + $N_i p$ (L2P) + $3^d N_i^2$ (P2P) Interior $n_c p^2$ (M2M) + $n_i p^2$ (M2L) + $n_c p^2$ (L2L)

Communication estimate:

```
Diagonal n_c(L-k-1)
  Lateral 2^{d} \frac{2^{m(L-k-1)}-1}{2^{m}-1} for incidence dimesion m
```

- Leverage existing work on graph partitioning
 - ParMetis



Why should a good partition exist?

Shang-hua Teng, Provably good partitioning and load balancing algorithms for parallel adaptive N-body simulation, SIAM J. Sci. Comput., 19(2), 1998.

- Good partitions exist for non-uniform distributions
 - 2D $\mathcal{O}(\sqrt{n}(\log n)^{3/2})$ edgecut
 - 3D $\mathcal{O}(n^{2/3}(\log n)^{4/3})$ edgecut
- As scalable as regular grids
- As efficient as uniform distributions
- ParMetis will find a nearly optimal partition



Will ParMetis find it?

George Karypis and Vipin Kumar, Analysis of Multilevel Graph Partitioning, Supercomputing, 1995.

- Good partitions exist for non-uniform distributions
 - 2D $C_i = 1.24^i C_0$ for random matching
 - 3D $C_i = 1.21^i C_0$?? for random matching
- 3D proof needs assurance that averge degree does not increase
- Efficient in practice

Parallel Tree Implementation Advantages

- Simplicity
- Complete serial code reuse
- Provably good performance and scalability

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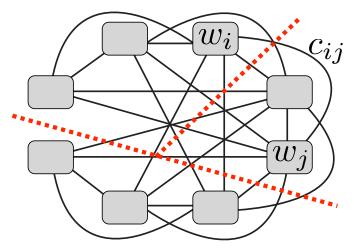
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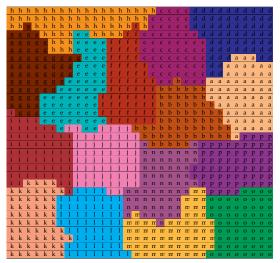
Distributing Local Trees

The interaction of locals trees is represented by a weighted graph.



This graph is partitioned, and trees assigned to processes.

Here local trees are assigned to processes:

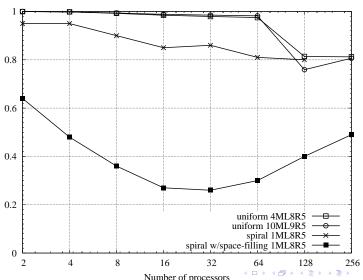


Parallel Data Movement

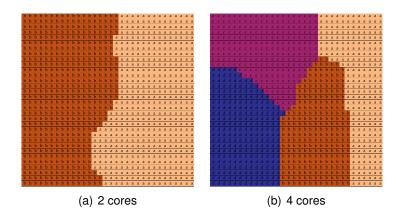
- Complete neighbor section
- Upward sweep
 - Upward sweep on local trees
 - Gather to root tree
 - Upward sweep on root tree
- Complete interaction list section
- Downward sweep
 - Downward sweep on root tree
 - Scatter to local trees
 - Oownward sweep on local trees

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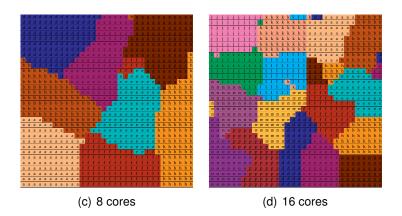
PetFMM Load Balance



Here local trees are assigned to processes for a spiral distribution:

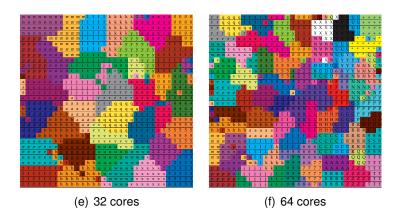


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GPU vs. CPU

A GPU looks like a big CPU with no virtual memory:

- Many more hardware threads encourage concurrency
- Makes bandwidth limitations even more acute
- Shared memory is really a user-managed cache
- Texture memory is also a specialized cache
- User also manages a very small code segment

GPU vs. CPU

Power usage can be very different:

Platform	TF	KW	GB/s	Price (\$)	GF/\$	GF/W
IBM BG/P	14	40.00	57.0*	1,800,000	0.008	0.35
IBM BlueGene	280	5000	???	350,000,000	0.0008	0.55
NVIDIA C1060	1	0.19	102.0	1,475	0.680	5.35
ATI 9250	1	0.12	63.5	840	1.220	8.33

Table: Comparison of Supercomputing Hardware.

Outline

- Multicore FMM
 - GPU Hardware
 - PetFMM



- In our C++ code on a CPU, M2L transforms take 85% of the time
 - This does vary depending on N
- New M2L design was implemented using PyCUDA
 - Port to C++ is underway
- We can now achieve 500 GF on the NVIDIA Tesla
 - Previous best performance we found was 100 GF
- We will release PetFMM-GPU in the new year



GPU M. Knepley (UC) Monash 46 / 49

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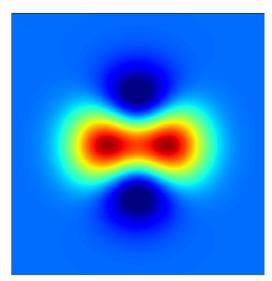


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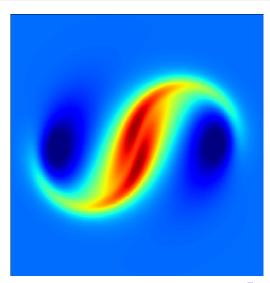
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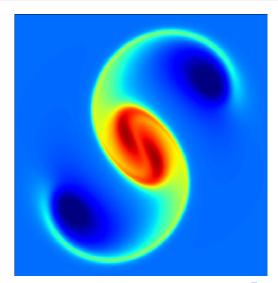


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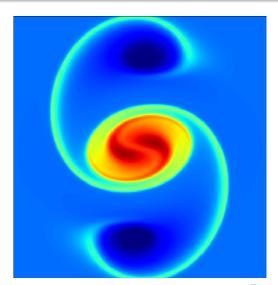


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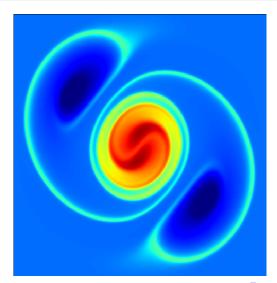




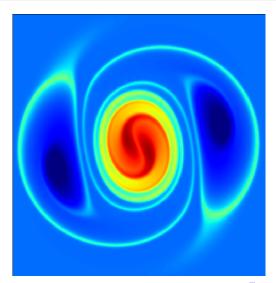




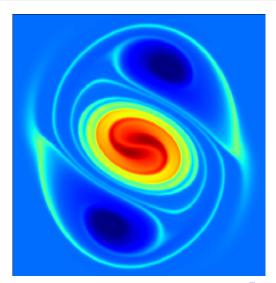




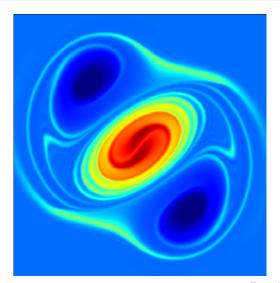




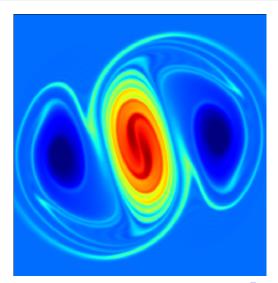














GPU Interaction

Since our parallelism is hierarchical

- Local (serial) tree interface is preserved
- GPU code can be reused locally without change
- Multiple GPUs per node can also be used



What's Important?

Interface improvements bring concrete benefits

- Facilitated code reuse
 - Serial code was largely reused
 - Test infrastructure completely reused
- Opportunites for performance improvement
 - Optimization using existing tools
 - Leverage GPU hardware
- Expansion of capabilities
 - Could now combine distributed and multicore implementations
 - Could replace local expansions with cheaper alternatives