

# Kolkata Algorithms Short Course: III-IV Parallel/Streamable Algorithms and Equation Solving

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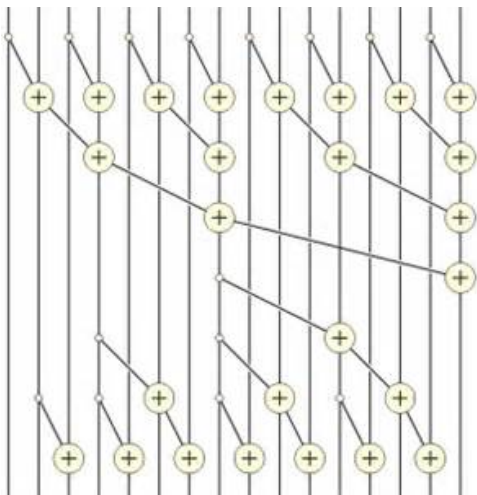
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- Another is that sorting has Boolean circuits a power of  $\log n$  in *depth*.



# Parallel Prefix Sum (PPS): Depth $2 \log n$



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- Wikipedia says this “inspired” the much more general “MapReduce” architecture for cloud computing, which retains the idea of a poly- $\log(n)$ -width stream. What it must *avoid* is  $\Omega(n)$ -width random access. Sorting and PPS give a toolkit.

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- Answer: use PPS to compose the maps  $g_c(q) = \delta(q, c)$  for each character;  $g_c \odot g_d = \text{take } q \text{ to } g_d(g_c(q))$  [show on board].

## Batcher's Bitonic Merge and Sort

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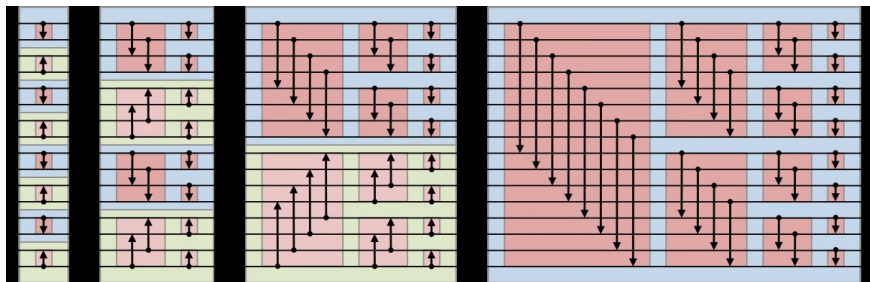
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- Gives Mergesort in  $O(n \log n)$  time with  $O((\log n)^2)$  depth.

## Python code from Wikipedia

```
def bitonic_merge(up, x): # assume input x is bitonic
    if len(x) == 1:
        return x
    else:
        bitonic_compare(up, x)
        first = bitonic_merge(up, x[:len(x) / 2])
        second = bitonic_merge(up, x[len(x) / 2:])
        return first + second

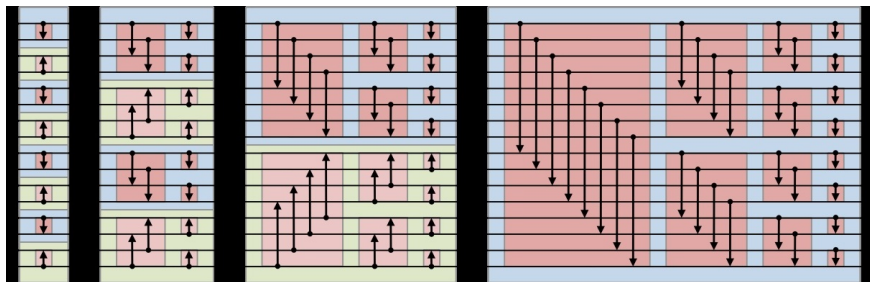
def bitonic_compare(up, x):
    dist = len(x) / 2
    for i in range(dist):
        if (x[i] > x[i+dist]) == up:
            x[i], x[i+dist] = x[i+dist], x[i] #swap
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## Picture (from Wikipedia)



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Thus one reason to care about the theoretical distinction of the “BFS class” is being able to make better parallel/cloud-friendly algorithms.

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- For equations the inspired guess is a solution; it is easy to check unless the math is too Complex.
- So 3SAT is in NP and basically so is equation solving—over  $\{0, 1\}$ -solutions anyway.

**Definition.** A decision problem  $B$  is *NP-hard* if for all problems  $A$  in NP there is a polynomial-time computable translation function  $f$  such that for all inputs  $x$  of problem  $A$ , the string  $y = f(x)$  is an equivalent input of problem  $B$ . And  $B$  is *NP-complete* if also  $B$  is in NP.

## Cook-Levin Theorem: 3SAT is NP-Complete

- Given  $A \in \text{NP}$  there is a *deterministic* TM  $M$  that verifies the relation “ $y$  is a lucky guess for  $x \in A$ ” in polynomial time.



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- The key is what we covered in day 2: the memory map of  $M$  can be converted into Boolean circuits  $C_n$ , one for each  $n$  (and the corresponding  $m$ ) such that  $M$  accepts  $(x, y)$  if and only if  $C_n(x, y) = 1$ . We can build  $C_n$  using only NAND gates.

## Finishing the Proof

- For each NAND gate  $g$ , let  $u_g$  and  $v_g$  be its two incoming wires (these can be inputs  $x_i$  or  $y_j$ ) and  $w_1, \dots, w_\ell$  its output wires.

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- So 3SAT is NP-hard, and since it is in NP, it is NP-complete.  $\square$

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- To finish that equation solving is NP-hard: for each NAND gate  $g$  with incoming wires  $u_g, v_g$  and outgoing wire  $w_g$  we give the equation

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The equations in this proof are indeed *very* simple—degree 2 for the  $u_g v_g$  terms and the Boolean equations. Does this really mean that solving them is hard in practice?

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## A Standard Greedy Heuristic Algorithm

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set<Clause> TODO = clauses(phi);
set<Variable> FREE = {x_1, ..., x_n}
while (TODO and FREE are both nonempty) {
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if (empty TODO) {
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Current “SAT Solvers” use more-sophisticated heuristics.

## Equation Solvers Use a Hammer

Represent a given set of pure-arithmetic equations abstractly as

$$p_1(z_1, \dots, z_n) = 0;$$

$$p_2(z_1, \dots, z_n) = 0;$$

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where each  $p_i$  is a multi-variable polynomial. Now observe:

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Represent a given set of pure-arithmetic equations abstractly as

$$\begin{aligned} p_1(z_1, \dots, z_n) &= 0; \\ p_2(z_1, \dots, z_n) &= 0; \\ &\vdots = 0; \\ p_s(z_1, \dots, z_n) &= 0; \end{aligned}$$

where each  $p_i$  is a multi-variable polynomial. Now observe:

For any polynomials  $q_1, \dots, q_s$  in the same variables  $\vec{z}$ , the polynomial

$$r(\vec{z}) = q_1(\vec{z})p_1(\vec{z}) + q_2(\vec{z})p_2(\vec{z}) + \dots + q_s(\vec{z})p_s(\vec{z})$$

must also be equated to 0. Call it an “algebraic consequence.”

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- But in many cases it finishes quickly enough, so people use it...

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