

Reading, Analyzing, and Simulating Quantum Circuits

(With speculation on the status of “quantum supremacy”)

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- So Math can outpace its own calculations, but can Nature?

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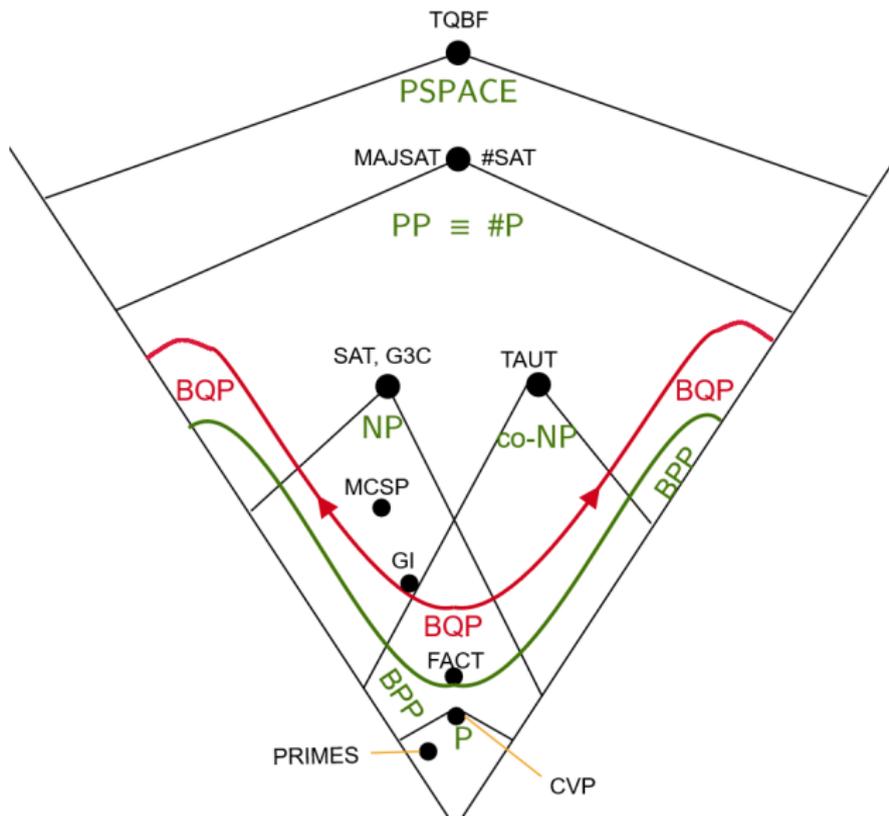
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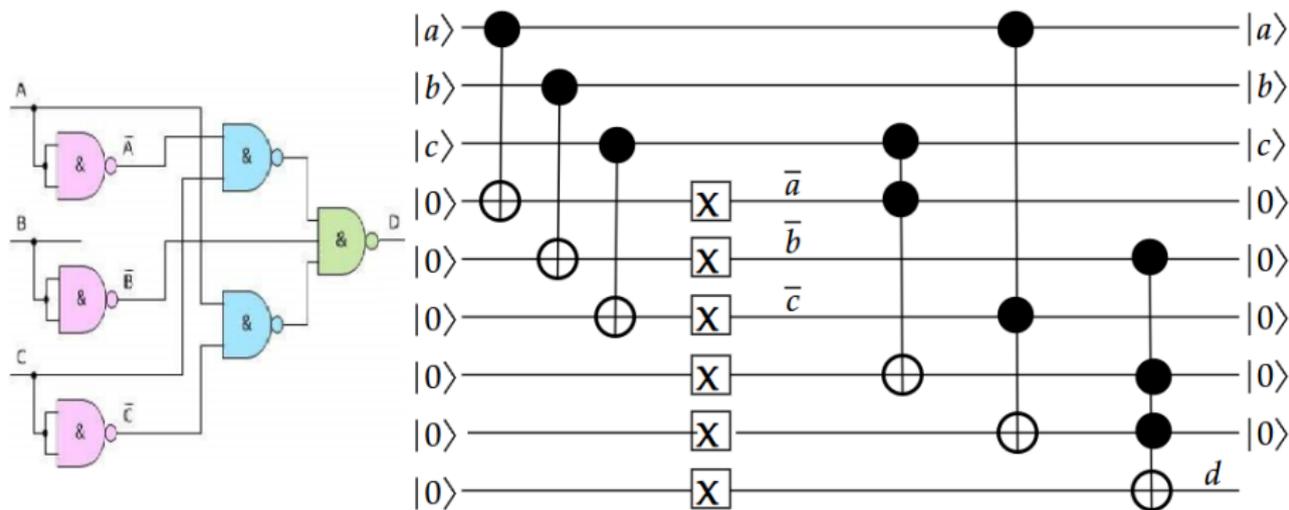
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- Realizable by **quantum circuits** of size $\tilde{O}(m^2)$.

The Complexity Class Neighborhood



Classical and Quantum Circuits

$d = f(a, b, c) = \bar{a} (c \bar{a} \bar{a}, \bar{b}, a \bar{a} \bar{c})$ $\mathbf{X} = \text{NOT}$, $\bullet \text{---} \oplus = \text{controlled-NOT}$



Quantum circuit computes the **reversible** Boolean function

$F(a, b, c, e_1, e_2, e_3, e_4, e_5, e_6) = (a, b, c, e_1, e_2, e_3, e_4, e_5, e_6 \oplus f(a, b, c))$.

Underlying: a vector of $N = 2^9 = 512$ dimensions.

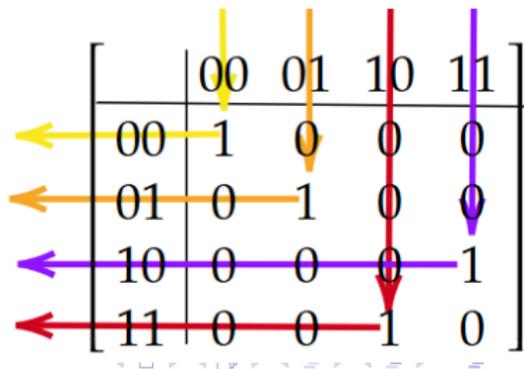
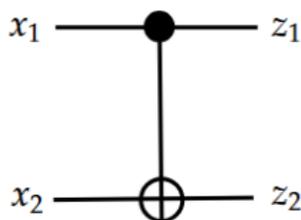
Quantum Coordinates and the CNOT Gate

By **linearity**, an n -qubit circuit is determined by its actions on the 0-1 **standard basis** vectors $e_{0^n} = [1, 0, 0, \dots, 0]^T$ thru $e_{1^n} = [0, 0, 0, \dots, 1]^T$.

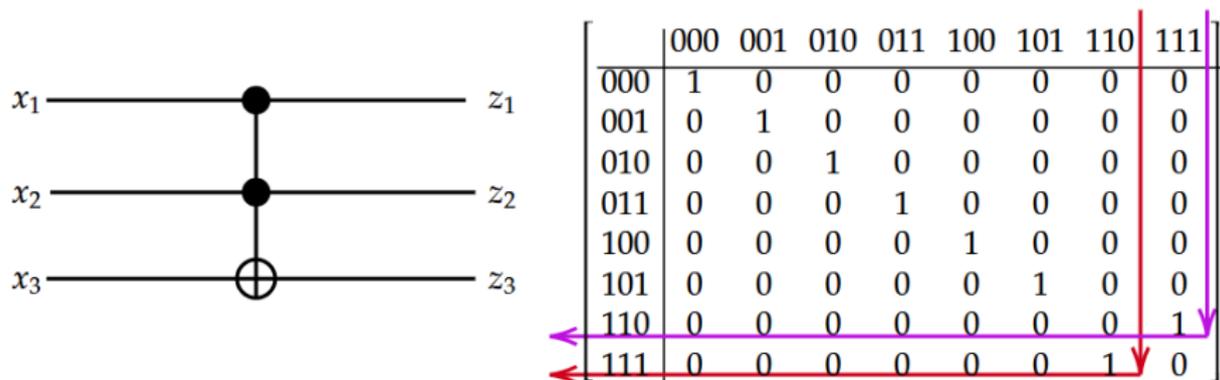
For $n = 2$, $e_{00} = \begin{bmatrix} 1 \\ 0 \\ 0 \\ 0 \end{bmatrix}$, $e_{01} = \begin{bmatrix} 0 \\ 1 \\ 0 \\ 0 \end{bmatrix}$, $e_{10} = \begin{bmatrix} 0 \\ 0 \\ 1 \\ 0 \end{bmatrix}$; and $e_{11} = \begin{bmatrix} 0 \\ 0 \\ 0 \\ 1 \end{bmatrix}$ (in

“big-endian” order). Then $\mathbf{CNOT}e_{00} = e_{00}$ and $\mathbf{CNOT}e_{01} = e_{01}$, while $\mathbf{CNOT}e_{10} = e_{11}$ and $\mathbf{CNOT}e_{11} = e_{10}$. **Feynman path** visualization:

$$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}$$



Toffoli Gate ($n = 3, N = 2^n = 8$)

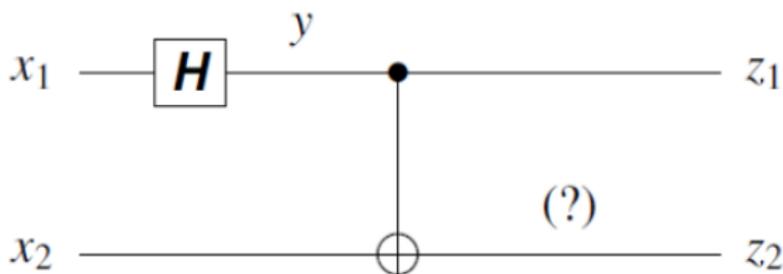


In **Dirac notation**, e_x is written $|x\rangle$. So we have **Tof** $|000\rangle = |000\rangle$ thru **Tof** $|101\rangle = |101\rangle$, while **Tof** $|110\rangle = |111\rangle$ and **Tof** $|111\rangle = |110\rangle$.

Note that fixing $x_3 = 1$ makes $z_3 = x_1 \bar{\wedge} x_2$. Since NAND is a universal gate, this already suffices to show that quantum circuits simulate classical ones. Their extra power comes from one more gate.

Hadamard Gate, Nondeterminism, and Entanglement

Hadamard gate $H = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix}$ “emits” a free Boolean variable y .



On input $|00\rangle$, $y = 0$ leads to output $z_1 = 0$, $z_2 = 0$, while $y = 1$ leads to $z_1 z_2 = 11$. The other combinations 01 and 10 cannot happen. *Matrices:*

$$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix} \cdot \begin{bmatrix} 1 & 0 & 1 & 0 \\ 0 & 1 & 0 & 1 \\ 1 & 0 & -1 & 0 \\ 0 & 1 & 0 & -1 \end{bmatrix} \cdot \begin{bmatrix} 1 \\ 0 \\ 0 \\ 0 \end{bmatrix} = \begin{bmatrix} 1 & 0 & 1 & 0 \\ 0 & 1 & 0 & 1 \\ 0 & 1 & 0 & -1 \\ 1 & 0 & -1 & 0 \end{bmatrix} \cdot \begin{bmatrix} 1 \\ 0 \\ 0 \\ 0 \end{bmatrix} = \begin{bmatrix} 1 \\ 0 \\ 0 \\ 1 \end{bmatrix}.$$

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- So an n -qubit circuit C , on any input $|x\rangle$, gives rise to a distribution $D_{C,x}$ on $\{0, 1\}^n$ to sample from.

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Measurement and Sampling

- **Measuring** the state vector $|\phi\rangle = [a_0, a_1, \dots, a_{N-1}]^T$ of all n qubits returns some i , $0 \leq i \leq N - 1$, with probability $|a_i|^2$. You can say it returns e_i , $|i\rangle$, or the i -th binary string $z_i \in \{0, 1\}^n$.
- This entails that $|\phi\rangle$ is a Euclidean **unit vector**, so that the probabilities sum to 1.
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- **Shor's algorithm** creates $D_{C,x}$ such that sampling often gives z from which the period r of $f_a(u) = a^u \bmod x$ can be classically inferred, which in turn often enables factoring x .

Some More Gates

$$X = \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}, \quad Y = \begin{bmatrix} 0 & -i \\ i & 0 \end{bmatrix}, \quad Z = \begin{bmatrix} 1 & 0 \\ 0 & -1 \end{bmatrix},$$

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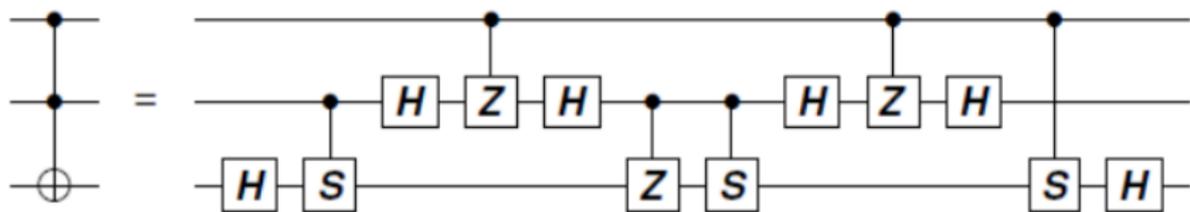
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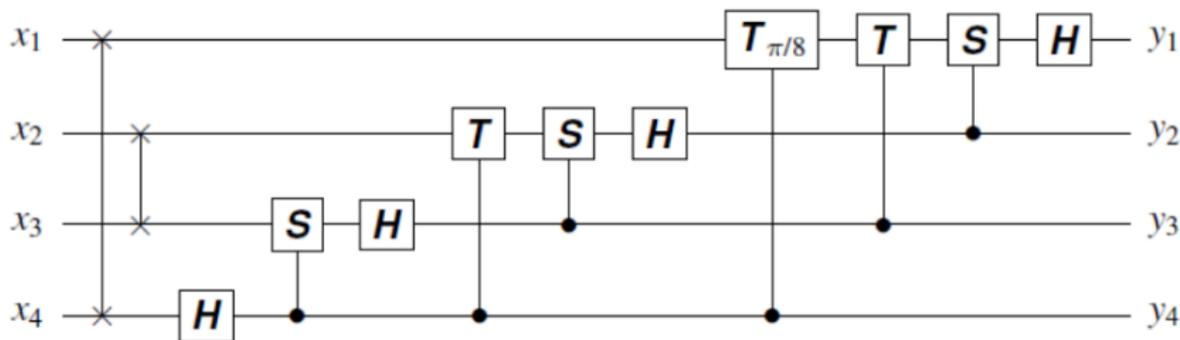
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- Adding any of T, R₈, CS, or Tof gives the full power of BQP.
- Note: T² = S, S² = Z, Z² = I = H², and CS² = CZ.

Two Notable Circuits

H and **CS** alone can simulate the Toffoli gate:



The 4-qubit **Quantum Fourier Transform** == the 16×16 **Discrete Fourier Transform**. In general, QFT_n needs $O(n^2)$ basic gates.



Juxtaposition and Tensor Product

Many QCs begin with m Hadamard gates on each of m qubits

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					0000	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
					0001	1	-1	1	-1	1	-1	1	-1	1	-1	1	-1	1	-1	1	-1	1
					0010	1	1	-1	-1	1	1	-1	-1	1	1	-1	-1	1	1	-1	-1	1
					0011	1	-1	-1	1	1	-1	1	-1	-1	1	-1	1	1	-1	-1	1	-1
$ x_2\rangle$	—	H	—		0100	1	1	1	1	-1	-1	-1	-1	1	1	1	1	-1	-1	-1	-1	1
					0101	1	-1	1	-1	-1	1	-1	1	1	-1	1	-1	-1	-1	1	-1	1
					0110	1	1	-1	-1	-1	-1	1	1	1	1	-1	-1	-1	-1	-1	1	1
					0111	1	-1	-1	1	-1	1	1	-1	1	-1	-1	1	-1	1	1	1	-1
$ x_3\rangle$	—	H	—		1000	1	1	1	1	1	1	1	1	1	-1	-1	-1	-1	-1	-1	-1	-1
					1001	1	-1	1	-1	1	-1	1	-1	-1	-1	1	-1	1	-1	1	-1	1
					1010	1	1	-1	-1	1	1	-1	-1	-1	-1	1	1	-1	-1	-1	1	1
					1011	1	-1	-1	1	1	-1	1	-1	-1	-1	1	1	-1	-1	1	1	-1
$ x_4\rangle$	—	H	—		1100	1	1	1	1	-1	-1	-1	-1	-1	-1	-1	-1	-1	1	1	1	1
					1101	1	-1	1	-1	-1	1	-1	1	-1	-1	1	-1	1	1	-1	1	-1
					1110	1	1	-1	-1	-1	-1	1	1	-1	-1	1	1	1	1	-1	-1	-1
				1111	1	-1	-1	1	-1	1	1	-1	-1	-1	1	1	-1	-1	-1	-1	1	

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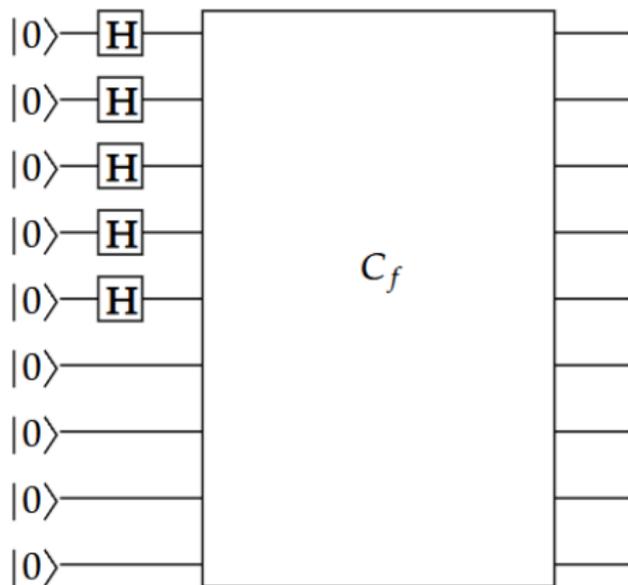
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Hadamard Transforms and Functions

Suppose $C_f(x, y) = (x, y \oplus f(x))$ computes the reversible form of f . Then

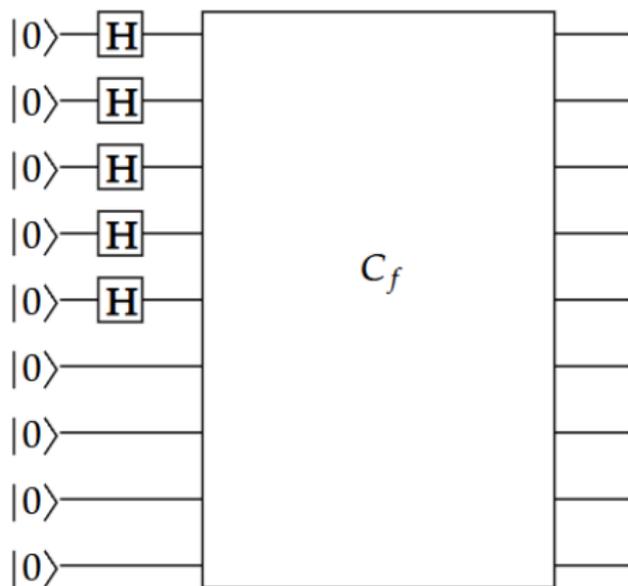


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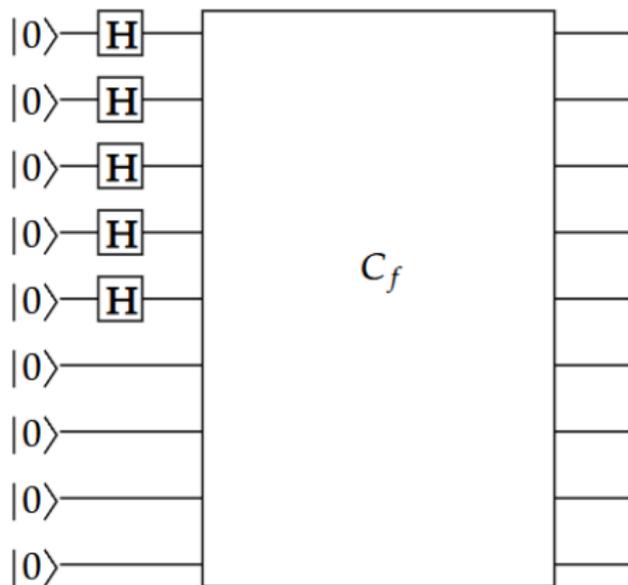
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Three Universal Libraries, Phase Angles, and Noise

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- The gate set $H + \text{CNOT} + T$ is **efficiently metrically universal**, meaning that any feasible quantum circuit of size s can be approximated to within entrywise error ϵ by a circuit of these gates only in size $O(s) \cdot (\log \frac{s}{\epsilon})^{O(1)}$. (See [Solovay-Kitaev theorem](#).)

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Demo of Simulation Code and Its Blue-Sky Ideas

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5. Iteratively program **tensor network** and **SVD** approximations...

On the agenda...

I. Feynman Path Polynomials and Logical Formulas

Let C have “minphase” $K = 2^k$ and let F embed K -th roots of unity ω .

- H + Tof has $k = 1$, $K = 2$.
- H + CS has $k = 2$, $K = 4$.
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Theorem (RC 2007-09, extending Dawson et al. (2004) over \mathbb{Z}_2)

Any QC C of n qubits quickly transforms into a polynomial $P_C = \prod_g P_g$ over gates g and a constant $R > 0$ such that for all $x, z \in \{0, 1\}^n$:

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where C has h nondeterministic (Hadamard) gates and $y \in \{0, 1\}^h$.

Additive Case (Cf. Bacon-van Dam-Russell [2008])

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Given C and K , we can efficiently compute a polynomial $Q_C(x_1, \dots, x_n, y_1, \dots, y_h, z_1, \dots, z_n, w_1, \dots, w_t)$ of *degree $O(1)$* over \mathbb{Z}_K and a constant R' such that for all $x, z \in \{0, 1\}^n$:

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- T-gate also goes cubic.

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Theorem (C. Guan in RCG 2018)

Given C, n, K, h as above, we can quickly build a Boolean formula ϕ_C in variables y_1, \dots, y_h , together with substituted-for $x_1, \dots, x_n, z_1, \dots, z_n$, and other “forced” variables such that for all $x, z \in \{0, 1\}^n$:

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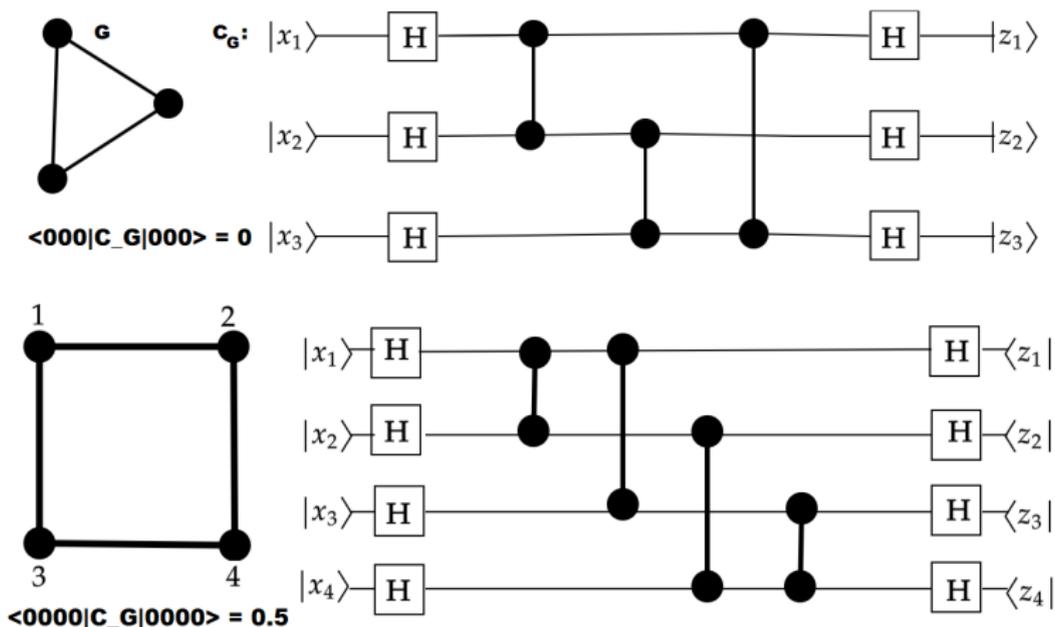
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- Easy to transform into 3CNF (i.e., “3SAT” form). **(show demo)**
- **For $K = 2, 4$** (i.e., for H + Tof and H + CS), we get the acceptance *probability* as a simple difference:

$$|\langle z \mid C \mid x \rangle|^2 = \frac{1}{R} (\#sat(\phi_C) - \#sat(\phi'_C)).$$

II. Strong Simulation of Graph State Circuits

Computing amplitudes $\langle z | C | x \rangle$ for Clifford circuits C can be efficiently reduced to computing $\langle 0^n | C_G | 0^n \rangle$ for **graph-state circuits** C_G of graphs G , using **H** and **CZ** gates, as exemplified by:



Improved From $O(n^3)$ to $O(n^{2.37155\dots})$

Theorem (Guan-Regan, 2019)

For n -qubit stabilizer circuits of size s , $\langle z | C | x \rangle$ can be computed in $O(s + n^\omega)$ time, where $\omega \leq 2.37155\dots$ is the exponent of multiplying $n \times n$ matrices.

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- **Corollary:** Counting solutions to quadratic polynomials $p(x_1, \dots, x_n)$ over \mathbb{Z}_2 is in $O(n^{2.37155\dots})$ time.
- Improves $O(n^3)$ time of **Ehrenfeucht-Karpinski (1990)**.

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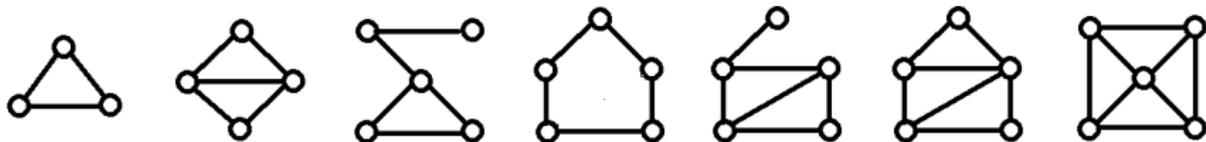
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Improved From $O(n^3)$ to $O(n^{2.37155\dots})$

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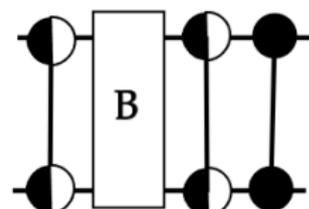
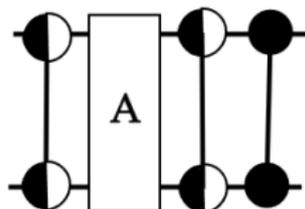
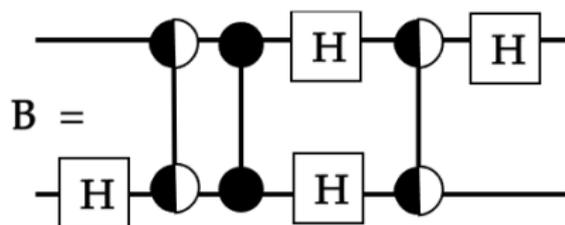
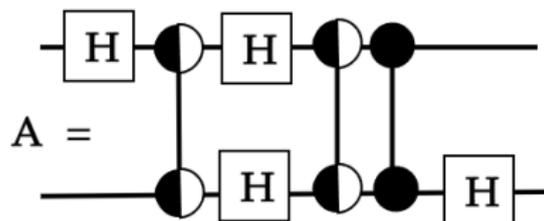
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- John Preskill’s [notes](#) show that the following four widgets, together with their conjugations by $H \otimes H$, suffice:



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- We [took them in a different direction](#).

Singular Value Decomposition

Unlike with diagonalization, this is *always* possible:

SVD Theorem: For every $m \times n$ matrix A we can efficiently find:

- an $m \times m$ unitary matrix U ,
- an $m \times n$ pseudo-diagonal matrix Σ with non-negative entries $\Sigma[i, i] = \sigma_i$, and
- an $n \times n$ unitary matrix V ,

such that $A = U\Sigma V^*$. Furthermore, we can arrange that $\sigma_1 \geq \sigma_2 \geq \dots \geq \sigma_{\min(m,n)}$, and in consequence:

- $\|A\|_F = \sqrt{\sum_i \sigma_i^2}$
- $\|A\|_2 = \sigma_1$,
- $A^*A = V\Sigma^T U^* U \Sigma V^* = V \operatorname{diag}(\sigma_i^2) V^*$, and
- $AA^* = U\Sigma V^* V \Sigma^T U^* = U \operatorname{diag}(\sigma_i^2) U^*$,

so that the squares of the σ_i and associated vectors give the spectral decompositions of the Hermitian PSD matrices A^*A and AA^* , respectively.

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- How this might be programmed.

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“It From Bit” we once proclaimed,
but now the Bit has bit the dust
of whizzing quantum chips that gamed
coherence, to evade the trust
that the Word framed creation’s hour:
Mother Nature fully lexical.
Why not evolve us that same power?
It is a status most perplexical.