

Sample Projects from some past semesters

11 bits floating point multiplication

16- bit D.E.S ENCRYPTION

Dynamic Reconfigurable TCAM

MNIST Classifier Using 6T SRAM Array

4x4 TCAM

8T SRAM Cell for performing Multi-bit Dot Product Engine

k-Nearest Neighbors algorithm using CMOS

16-bit single precision floating-point decimal adder

Comparison of Adders Optimized for High-Speed and Low Energy in ALU Design

Dual Match line Ternary Content Addressable Memory

6T, 7T and 8T SRAM comparison

PHASE LOCKED LOOPS

4 BIT ALU WITH CARRY LOOKAHEAD ADDER AND MULTIPLIER

10T SRAM Cell for High SNM and Low Power

Straight Permutation Box

REDUCTION OF DYNAMIC POWER CONSUMPTION USING RIPPLE PRE-CHARGE TCAM

Design of a Vending Machine Controller

8-bit floating point multiplier

Using the Kogge Stone Adder to Implement Vedic Multiplier

Traffic Controller System

SECURE AND ENERGY EFFICIENT ARBITER BASED PUF

Design of TCAM for IP-Lookup Using Two Side Self Gating Technique

DESIGN OF S-BOX USING COMBINATIONAL LOGIC