

CSE 431/531: Algorithm Analysis and Design (Spring 2020)

NP-Completeness

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NP-Completeness Theory

- The topics we discussed so far are **positive results**: how to design efficient algorithms for solving a given problem.
- NP-Completeness provides **negative results**: some problems can **not** be solved efficiently.

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Q: Why do we study negative results?

- A given problem X cannot be solved in polynomial time.
- Without knowing it, you will have to keep trying to find polynomial time algorithm for solving X . All our efforts are doomed!

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- Do not need to worry about the computational model

Outline

- 1 Some Hard Problems
- 2 P, NP and Co-NP
- 3 Polynomial Time Reductions and NP-Completeness
- 4 NP-Complete Problems
- 5 Dealing with NP-Hard Problems
- 6 Summary

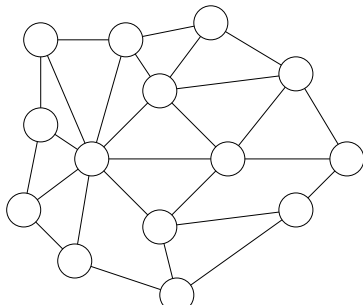
Example: Hamiltonian Cycle Problem

Def. Let G be an undirected graph. A **Hamiltonian Cycle (HC)** of G is a cycle C in G that **passes each vertex of G exactly once**.

Hamiltonian Cycle (HC) Problem

Input: graph $G = (V, E)$

Output: whether G contains a Hamiltonian cycle



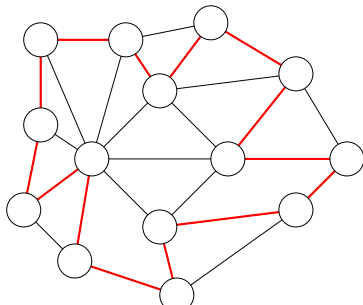
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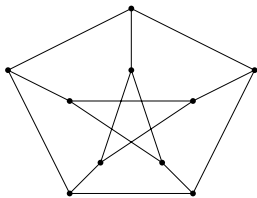
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- The graph is called the **Petersen Graph**. It has no HC.

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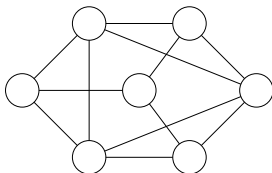
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- HC is **NP-hard**: it is **unlikely** that it can be solved in polynomial time.

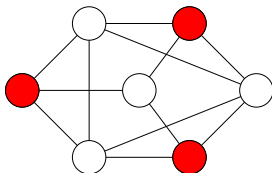
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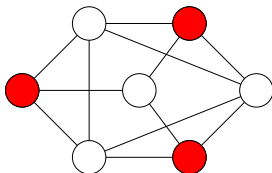
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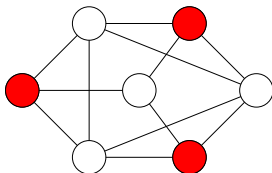
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- Maximum Independent Set is NP-hard

Formula Satisfiability

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Input: boolean formula with n variables, with \vee, \wedge, \neg operators.

Output: whether the boolean formula is satisfiable

- Example: $\neg((\neg x_1 \wedge x_2) \vee (\neg x_1 \wedge \neg x_3) \vee x_1 \vee (\neg x_2 \wedge x_3))$ is not satisfiable
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Fact For each optimization problem X , there is a decision version X' of the problem. If we have a polynomial time algorithm for the decision version X' , we can solve the original problem X in polynomial time.

Optimization to Decision

Shortest Path

Input: graph $G = (V, E)$, weight w , s, t and a bound L

Output: whether there is a path from s to t of length at most L

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Input: a graph G and a bound k

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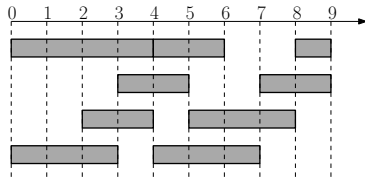
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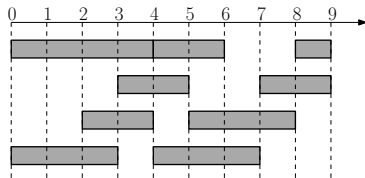
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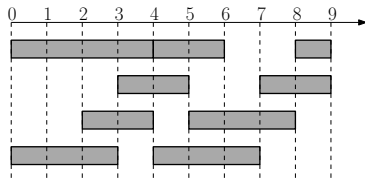


- $(0, 3, 0, 4, 2, 4, 3, 5, 4, 6, 4, 7, 5, 8, 7, 9, 8, 9)$

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Example: Interval Scheduling Problem



- $(0, 3, 0, 4, 2, 4, 3, 5, 4, 6, 4, 7, 5, 8, 7, 9, 8, 9)$
- Encode the sequence into a binary string as before

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A: No! As long as we are using a “natural” encoding. We only care whether the running time is polynomial or not

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Def. A has a **polynomial running time** if there is a polynomial function $p(\cdot)$ so that for every string s , the algorithm A terminates on s in at most $p(|s|)$ steps.

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- The decision versions of interval scheduling, shortest path and minimum spanning tree all in P.

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Def. The message Alice sends to Bob is called a **certificate**, and the algorithm Bob runs is called a **certifier**.

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- Certifier: check if the given set is really an independent set

Graph Isomorphism

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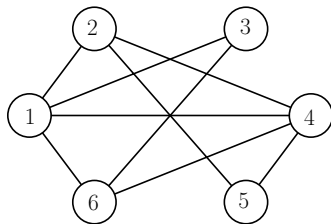
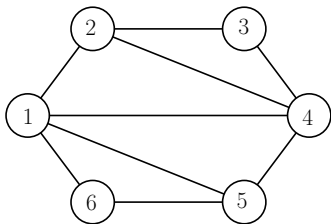
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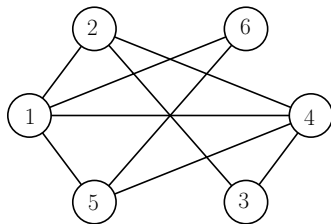
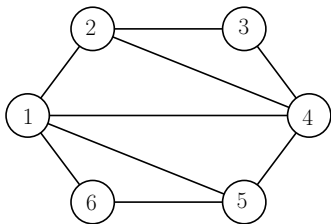


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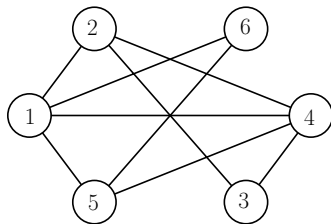
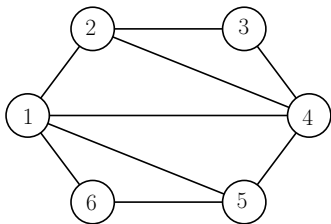


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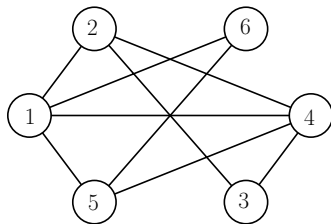
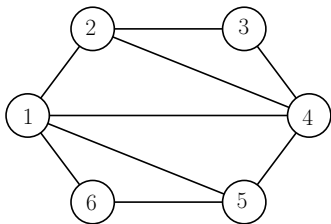
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- What is the certificate?
- What is the certifier?

The Complexity Class NP

Def. B is an **efficient certifier** for a problem X if

- B is a polynomial-time algorithm that takes two input strings s and t
- there is a polynomial function p such that, $s \in X$ if and only if there is string t such that $|t| \leq p(|s|)$ and $B(s, t) = 1$.

The string t such that $B(s, t) = 1$ is called a **certificate**.

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- $G \in \text{HC} \iff \exists S, B(G, S) = 1$

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- $|\text{encoding}(f)| \leq p(|\text{encoding}(G_1, G_2)|)$ for some polynomial function p
- Certifier B : $B((G_1, G_2), f) = 1$ if and only if for every $u, v \in V$, we have $(u, v) \in E_1 \Leftrightarrow (f(u), f(v)) \in E_2$.
- Clearly, B runs in polynomial time
- $(G_1, G_2) \in \text{GI} \iff \exists f, B((G_1, G_2), f) = 1$

Maximum Independent Set \in NP

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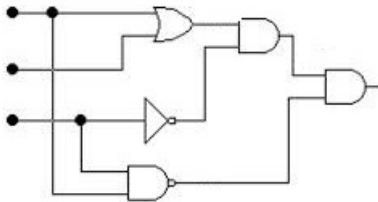
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Circuit Satisfiability (Circuit-Sat) Problem

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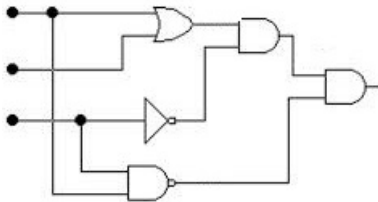
Output: whether there is an assignment such that the output is 1?



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- Is Circuit-Sat \in NP?

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The Complexity Class Co-NP

Def. For a problem X , the problem \overline{X} is the problem such that $s \in \overline{X}$ if and only if $s \notin X$.

Def. **Co-NP** is the set of decision problems X such that $\overline{X} \in \text{NP}$.

Def. A **tautology** is a boolean formula that always evaluates to 1.

Tautology Problem

Input: a boolean formula

Output: whether the formula is a tautology

- e.g. $(\neg x_1 \wedge x_2) \vee (\neg x_1 \wedge \neg x_3) \vee x_1 \vee (\neg x_2 \wedge x_3)$ is a tautology

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- Indeed, Tautology = $\overline{\text{Formula-Unsat}}$

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- Similarly, $P \subseteq Co-NP$, thus $P \subseteq NP \cap Co-NP$

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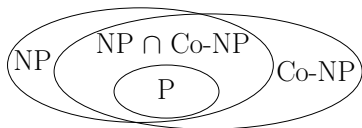
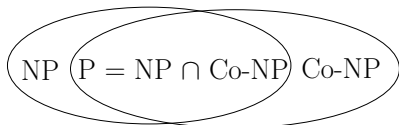
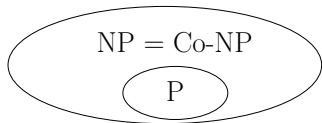
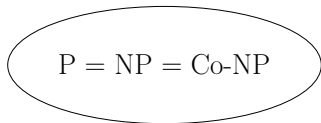
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Is $NP = Co-NP$?

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- General belief: $NP \neq Co-NP$.

4 Possibilities of Relationships

Notice that $X \in \text{NP} \iff \bar{X} \in \text{Co-NP}$ and $P \subseteq \text{NP} \cap \text{Co-NP}$



- General belief: we are in the 4th scenario

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- 2 P, NP and Co-NP
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Polynomial-Time Reductions

Def. Given a black box algorithm A that solves a problem X , if any instance of a problem Y can be solved using a polynomial number of standard computational steps, plus a polynomial number of calls to A , then we say Y is polynomial-time reducible to X , denoted as $Y \leq_P X$.

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To prove negative results:

Suppose $Y \leq_P X$. If Y cannot be solved in polynomial time, then X cannot be solved in polynomial time.

Polynomial-Time Reduction: Example

Hamiltonian-Path (HP) problem

Input: $G = (V, E)$ and $s, t \in V$

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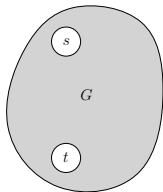
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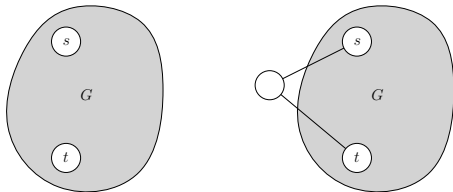
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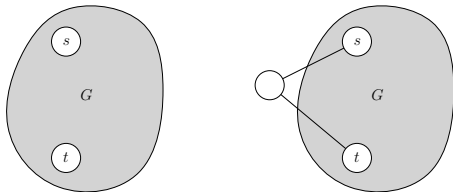
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Obs. G has a HP from s to t if and only if graph on right side has a HC.

NP-Completeness

Def. A problem X is called **NP-complete** if

- 1 $X \in \text{NP}$, and
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NP-Completeness

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- If you believe $\text{P} \neq \text{NP}$, and proved that a problem X is NP-complete (or NP-hard), stop trying to design efficient algorithms for X

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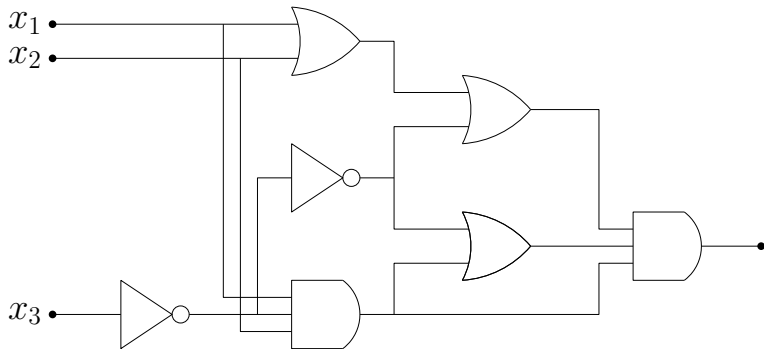
- How can we find a problem $X \in \text{NP}$ such that every problem $Y \in \text{NP}$ is polynomial time reducible to X ? Are we asking for too much?
- No! There is indeed a large family of natural NP-complete problems

The First NP-Complete Problem: Circuit-Sat

Circuit Satisfiability (Circuit-Sat)

Input: a circuit

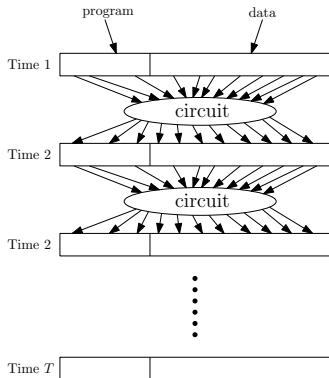
Output: whether the circuit is satisfiable



Circuit-Sat is NP-Complete

- key fact: algorithms can be converted to circuits

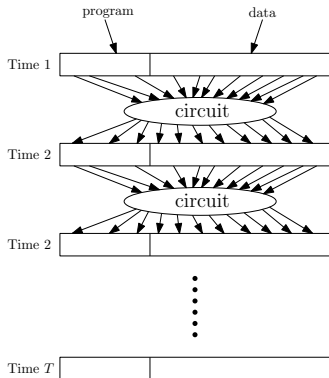
Fact Any algorithm that takes n bits as input and outputs 0/1 with running time $T(n)$ can be converted into a circuit of size $p(T(n))$ for some polynomial function $p(\cdot)$.



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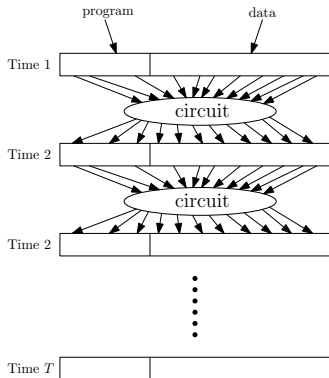


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- Then, we can show that any problem $Y \in \text{NP}$ can be reduced to Circuit-Sat.
- We prove $\text{HC} \leq_P \text{Circuit-Sat}$ as an example.

HC \leq_P Circuit-Sat

check-HC(G, S)

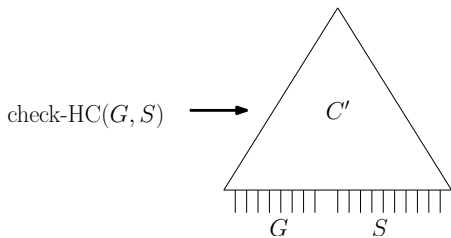
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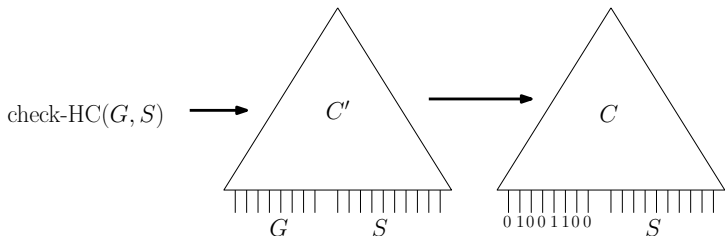
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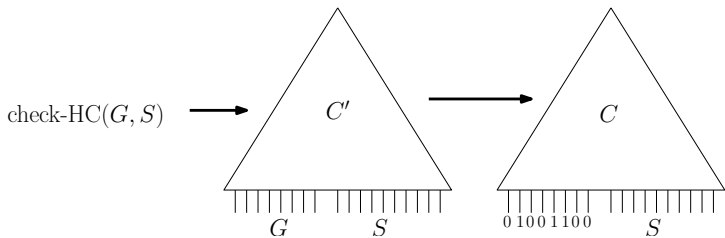
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$Y \leq_P \text{Circuit-Sat}$, For Every $Y \in \text{NP}$

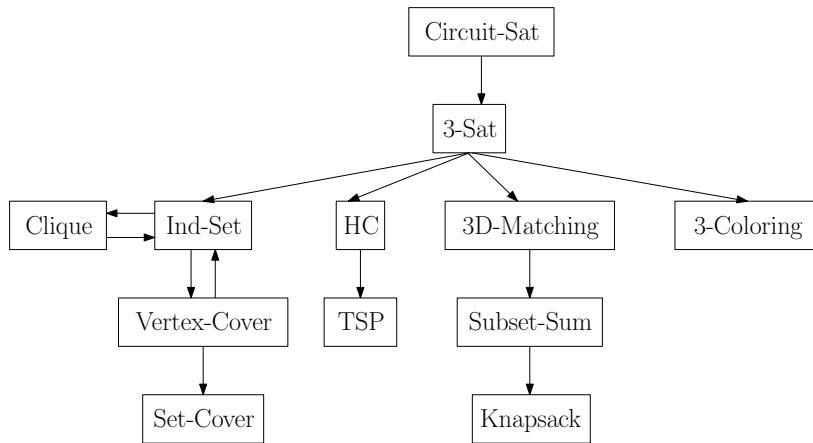
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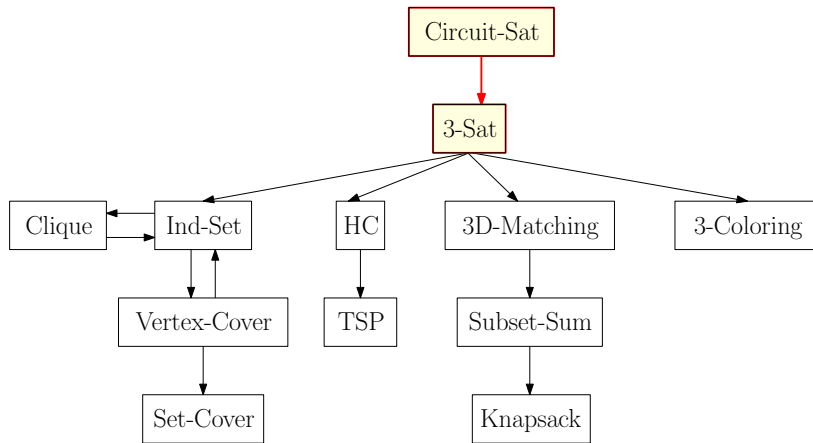
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- Construct a circuit C' for the algorithm $\text{check-}Y$
- hard-wire the instance s to the circuit C' to obtain the circuit C
- s is a yes-instance if and only if C is satisfiable □

Theorem Circuit-Sat is NP-complete.

Reductions of NP-Complete Problems



Reductions of NP-Complete Problems



3-CNF (conjunctive normal form) is a special case of formula:

3-Sat

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- Clause: disjunction (“or”) of at most 3 literals: $x_3 \vee \neg x_4,$
 $x_1 \vee x_8 \vee \neg x_9, \quad \neg x_2 \vee \neg x_5 \vee x_7$

3-CNF (conjunctive normal form) is a special case of formula:

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- Clause: disjunction (“or”) of at most 3 literals: $x_3 \vee \neg x_4, x_1 \vee x_8 \vee \neg x_9, \neg x_2 \vee \neg x_5 \vee x_7$
- 3-CNF formula: conjunction (“and”) of clauses:
 $(x_1 \vee \neg x_2 \vee \neg x_3) \wedge (x_2 \vee x_3 \vee x_4) \wedge (\neg x_1 \vee \neg x_3 \vee \neg x_4)$

3-Sat

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Input: a 3-CNF formula

Output: whether the 3-CNF is satisfiable

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3-Sat

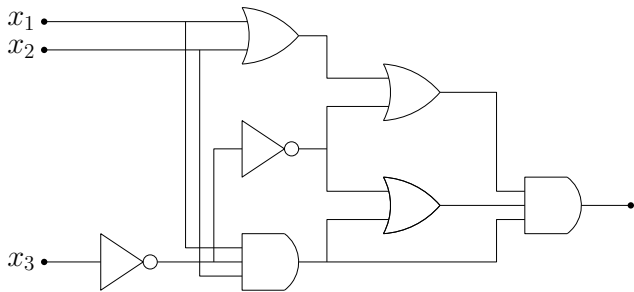
3-Sat

Input: a 3-CNF formula

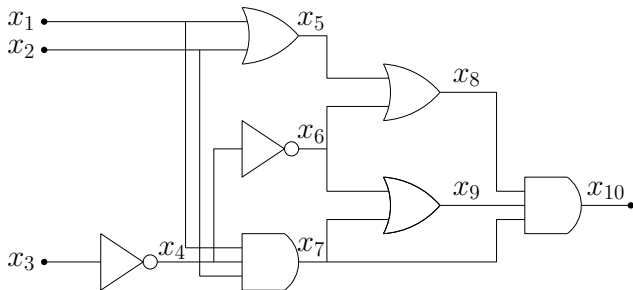
Output: whether the 3-CNF is satisfiable

- To satisfy a 3-CNF, we need to satisfy all clauses
- To satisfy a clause, we need to satisfy at least 1 literal
- Assignment $x_1 = 1, x_2 = 1, x_3 = 0, x_4 = 0$ satisfies
 $(x_1 \vee \neg x_2 \vee \neg x_3) \wedge (x_2 \vee x_3 \vee x_4) \wedge (\neg x_1 \vee \neg x_3 \vee \neg x_4)$

Circuit-Sat \leq_P 3-Sat

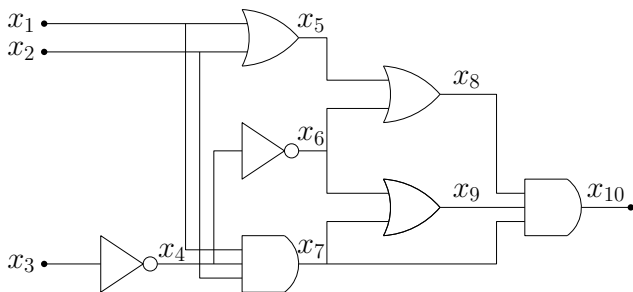


Circuit-Sat \leq_P 3-Sat



- Associate every wire with a new variable

Circuit-Sat \leq_P 3-Sat



- Associate every wire with a new variable
- The circuit is equivalent to the following formula:

$$\begin{aligned} & (x_4 = \neg x_3) \wedge (x_5 = x_1 \vee x_2) \wedge (x_6 = \neg x_4) \\ & \wedge (x_7 = x_1 \wedge x_2 \wedge x_4) \wedge (x_8 = x_5 \vee x_6) \\ & \wedge (x_9 = x_6 \vee x_9) \wedge (x_{10} = x_8 \wedge x_9 \wedge x_7) \wedge x_{10} \end{aligned}$$

Circuit-Sat \leq_P 3-Sat

$$\begin{aligned} & (x_4 = \neg x_3) \wedge (x_5 = x_1 \vee x_2) \wedge (x_6 = \neg x_4) \\ & \wedge (x_7 = x_1 \wedge x_2 \wedge x_4) \wedge (x_8 = x_5 \vee x_6) \\ & \wedge (x_9 = x_6 \vee x_9) \wedge (x_{10} = x_8 \wedge x_9 \wedge x_7) \wedge x_{10} \end{aligned}$$

Convert each clause to a 3-CNF

Circuit-Sat \leq_P 3-Sat

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Convert each clause to a 3-CNF

$$x_5 = x_1 \vee x_2 \quad \Leftrightarrow$$

x_1	x_2	x_5	$x_5 \leftrightarrow x_1 \vee x_2$
0	0	0	1
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

Circuit-Sat \leq_P 3-Sat

$$\begin{aligned} &(x_4 = \neg x_3) \wedge (x_5 = x_1 \vee x_2) \wedge (x_6 = \neg x_4) \\ &\wedge (x_7 = x_1 \wedge x_2 \wedge x_4) \wedge (x_8 = x_5 \vee x_6) \\ &\wedge (x_9 = x_6 \vee x_9) \wedge (x_{10} = x_8 \wedge x_9 \wedge x_7) \wedge x_{10} \end{aligned}$$

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0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

Circuit-Sat \leq_P 3-Sat

$$\begin{aligned} & (x_4 = \neg x_3) \wedge (x_5 = x_1 \vee x_2) \wedge (x_6 = \neg x_4) \\ & \wedge (x_7 = x_1 \wedge x_2 \wedge x_4) \wedge (x_8 = x_5 \vee x_6) \\ & \wedge (x_9 = x_6 \vee x_9) \wedge (x_{10} = x_8 \wedge x_9 \wedge x_7) \wedge x_{10} \end{aligned}$$

Convert each clause to a 3-CNF

$$x_5 = x_1 \vee x_2 \quad \Leftrightarrow$$

$$(x_1 \vee x_2 \vee \neg x_5) \quad \wedge$$

x_1	x_2	x_5	$x_5 \leftrightarrow x_1 \vee x_2$
0	0	0	1
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

Circuit-Sat \leq_P 3-Sat

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Convert each clause to a 3-CNF

	x_1	x_2	x_5	$x_5 \leftrightarrow x_1 \vee x_2$
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$x_5 = x_1 \vee x_2 \quad \Leftrightarrow$	0	0	1	0
	0	1	0	0
$(x_1 \vee x_2 \vee \neg x_5) \quad \wedge$	0	1	1	1
	1	0	0	0
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Circuit-Sat \leq_P 3-Sat

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$x_5 = x_1 \vee x_2 \quad \Leftrightarrow$	0	0	1	0
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$(x_1 \vee x_2 \vee \neg x_5) \quad \wedge$	0	1	1	1
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	x_1	x_2	x_5	$x_5 \leftrightarrow x_1 \vee x_2$
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$x_5 = x_1 \vee x_2 \quad \Leftrightarrow$	0	0	1	0
	0	1	0	0
$(x_1 \vee x_2 \vee \neg x_5) \quad \wedge$	0	1	1	1
$(x_1 \vee \neg x_2 \vee x_5) \quad \wedge$	1	0	0	0
	1	0	1	1
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Circuit-Sat \leq_P 3-Sat

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$x_5 = x_1 \vee x_2 \quad \Leftrightarrow$	0	0	1	0
	0	1	0	0
$(x_1 \vee x_2 \vee \neg x_5) \quad \wedge$	0	1	1	1
$(x_1 \vee \neg x_2 \vee x_5) \quad \wedge$	1	0	0	0
$(\neg x_1 \vee x_2 \vee x_5) \quad \wedge$	1	0	1	1
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Circuit-Sat \leq_P 3-Sat

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	0	1	0	0
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$(x_1 \vee \neg x_2 \vee x_5) \quad \wedge$	1	0	0	0
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Convert each clause to a 3-CNF

	x_1	x_2	x_5	$x_5 \leftrightarrow x_1 \vee x_2$
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$x_5 = x_1 \vee x_2 \iff$	0	0	1	0
	0	1	0	0
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Circuit-Sat \leq_P 3-Sat

- Circuit \iff Formula \iff 3-CNF

Circuit-Sat \leq_P 3-Sat

- Circuit \iff Formula \iff 3-CNF
- The circuit is satisfiable if and only if the 3-CNF is satisfiable

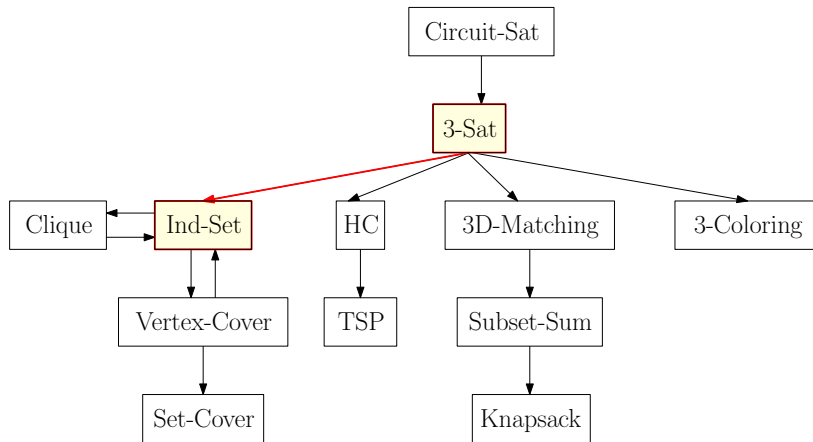
Circuit-Sat \leq_P 3-Sat

- Circuit \iff Formula \iff 3-CNF
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- The size of the 3-CNF formula is polynomial (indeed, linear) in the size of the circuit

Circuit-Sat \leq_P 3-Sat

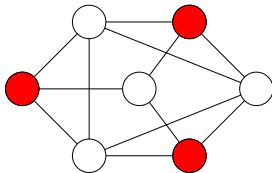
- Circuit \iff Formula \iff 3-CNF
- The circuit is satisfiable if and only if the 3-CNF is satisfiable
- The size of the 3-CNF formula is polynomial (indeed, linear) in the size of the circuit
- Thus, Circuit-Sat \leq_P 3-Sat

Reductions of NP-Complete Problems



Recall: Independent Set Problem

Def. An **independent set** of $G = (V, E)$ is a subset $I \subseteq V$ such that no two vertices in I are adjacent in G .



Independent Set (Ind-Set) Problem

Input: $G = (V, E), k$

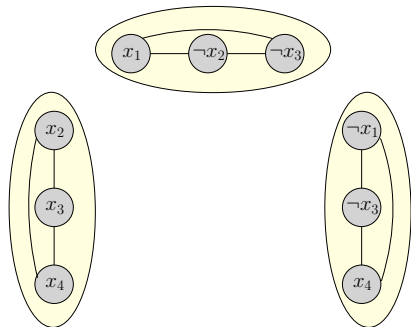
Output: whether there is an independent set of size k in G

3-Sat \leq_P Ind-Set

- $(x_1 \vee \neg x_2 \vee \neg x_3) \wedge (x_2 \vee x_3 \vee x_4) \wedge (\neg x_1 \vee \neg x_3 \vee x_4)$

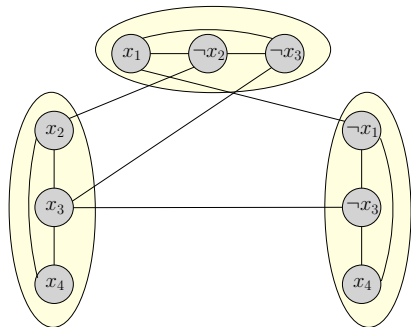
3-Sat \leq_P Ind-Set

- $(x_1 \vee \neg x_2 \vee \neg x_3) \wedge (x_2 \vee x_3 \vee x_4) \wedge (\neg x_1 \vee \neg x_3 \vee x_4)$
- A clause \Rightarrow a group of 3 vertices, one for each literal
- An edge between every pair of vertices in same group



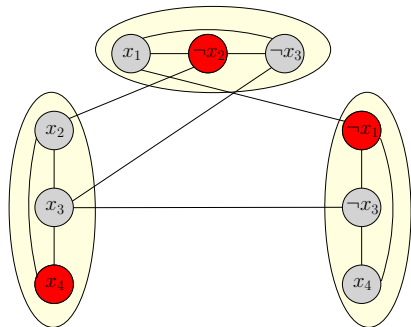
3-Sat \leq_P Ind-Set

- $(x_1 \vee \neg x_2 \vee \neg x_3) \wedge (x_2 \vee x_3 \vee x_4) \wedge (\neg x_1 \vee \neg x_3 \vee x_4)$
- A clause \Rightarrow a group of 3 vertices, one for each literal
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- An edge between every pair of contradicting literals



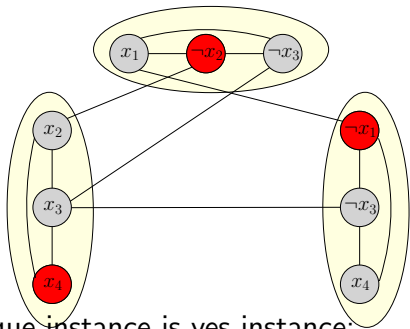
3-Sat \leq_P Ind-Set

- $(x_1 \vee \neg x_2 \vee \neg x_3) \wedge (x_2 \vee x_3 \vee x_4) \wedge (\neg x_1 \vee \neg x_3 \vee x_4)$
- A clause \Rightarrow a group of 3 vertices, one for each literal
- An edge between every pair of vertices in same group
- An edge between every pair of contradicting literals
- Problem: whether there is an IS of size $k = \#\text{clauses}$



3-Sat \leq_P Ind-Set

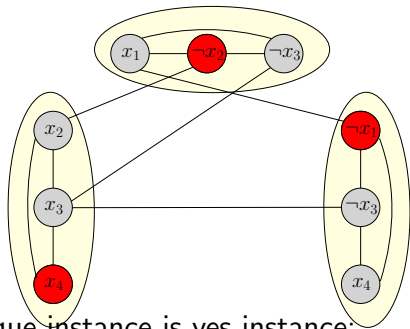
- $(x_1 \vee \neg x_2 \vee \neg x_3) \wedge (x_2 \vee x_3 \vee x_4) \wedge (\neg x_1 \vee \neg x_3 \vee x_4)$
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- Problem: whether there is an IS of size $k = \#$ clauses



3-Sat instance is yes-instance \Leftrightarrow clique instance is yes-instance:

3-Sat \leq_P Ind-Set

- $(x_1 \vee \neg x_2 \vee \neg x_3) \wedge (x_2 \vee x_3 \vee x_4) \wedge (\neg x_1 \vee \neg x_3 \vee x_4)$
- A clause \Rightarrow a group of 3 vertices, one for each literal
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- Problem: whether there is an IS of size $k = \#$ clauses

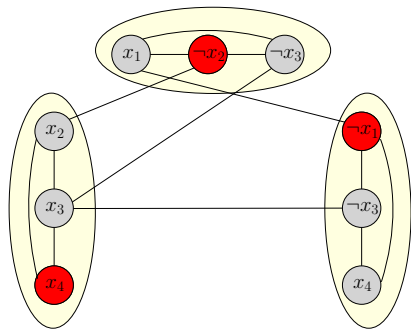


3-Sat instance is yes-instance \Leftrightarrow clique instance is yes-instance:

- satisfying assignment \Rightarrow independent set of size k
- independent set of size $k \Rightarrow$ satisfying assignment

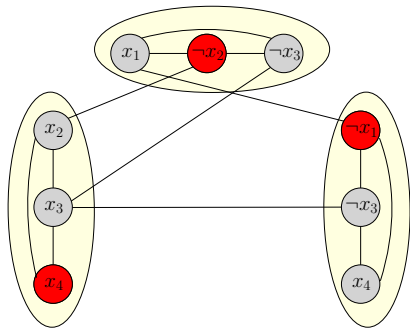
Satisfying Assignment \Rightarrow IS of Size k

- $(x_1 \vee \neg x_2 \vee \neg x_3) \wedge (x_2 \vee x_3 \vee x_4) \wedge (\neg x_1 \vee \neg x_3 \vee x_4)$



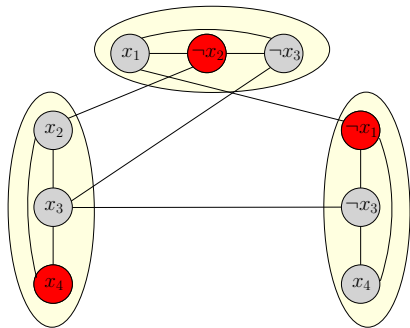
Satisfying Assignment \Rightarrow IS of Size k

- $(x_1 \vee \neg x_2 \vee \neg x_3) \wedge (x_2 \vee x_3 \vee x_4) \wedge (\neg x_1 \vee \neg x_3 \vee x_4)$
- For every clause, at least 1 literal is satisfied



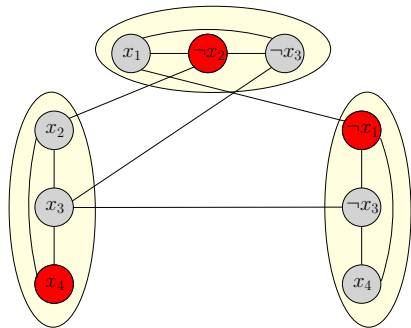
Satisfying Assignment \Rightarrow IS of Size k

- $(x_1 \vee \neg x_2 \vee \neg x_3) \wedge (x_2 \vee x_3 \vee x_4) \wedge (\neg x_1 \vee \neg x_3 \vee x_4)$
- For every clause, at least 1 literal is satisfied
- Pick the vertex correspondent the literal



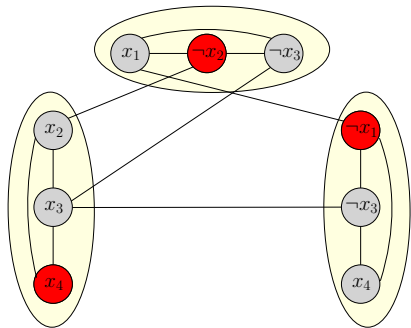
Satisfying Assignment \Rightarrow IS of Size k

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- For every clause, at least 1 literal is satisfied
- Pick the vertex correspondent the literal
- So, 1 literal from each group



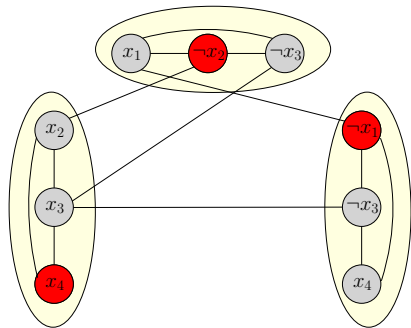
Satisfying Assignment \Rightarrow IS of Size k

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- For every clause, at least 1 literal is satisfied
- Pick the vertex correspondent the literal
- So, 1 literal from each group
- No contradictions among the selected literals



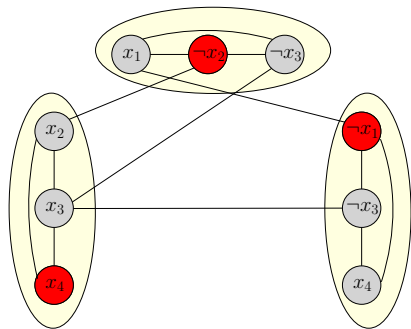
Satisfying Assignment \Rightarrow IS of Size k

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- For every clause, at least 1 literal is satisfied
- Pick the vertex correspondent the literal
- So, 1 literal from each group
- No contradictions among the selected literals
- An IS of size k



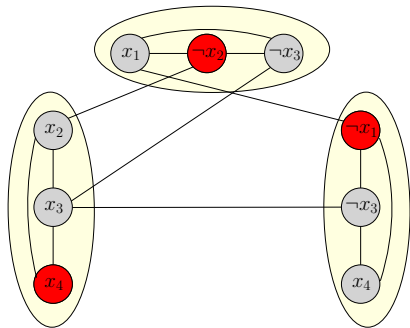
IS of Size $k \Rightarrow$ Satisfying Assignment

- $(x_1 \vee \neg x_2 \vee \neg x_3) \wedge (x_2 \vee x_3 \vee x_4) \wedge (\neg x_1 \vee \neg x_3 \vee x_4)$



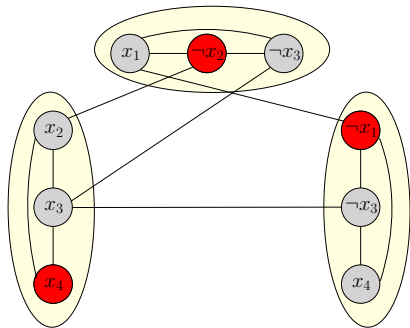
IS of Size $k \Rightarrow$ Satisfying Assignment

- $(x_1 \vee \neg x_2 \vee \neg x_3) \wedge (x_2 \vee x_3 \vee x_4) \wedge (\neg x_1 \vee \neg x_3 \vee x_4)$
- For every group, exactly one literal is selected in IS



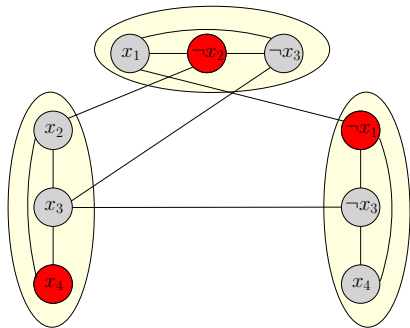
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- For every group, exactly one literal is selected in IS
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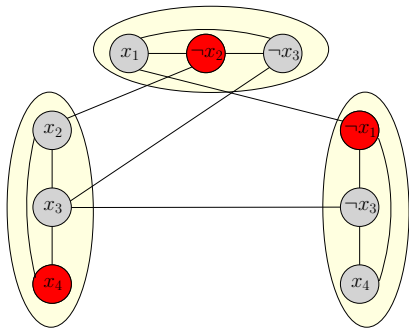
IS of Size $k \Rightarrow$ Satisfying Assignment

- $(x_1 \vee \neg x_2 \vee \neg x_3) \wedge (x_2 \vee x_3 \vee x_4) \wedge (\neg x_1 \vee \neg x_3 \vee x_4)$
- For every group, exactly one literal is selected in IS
- No contradictions among the selected literals
- If x_i is selected in IS, set $x_i = 1$



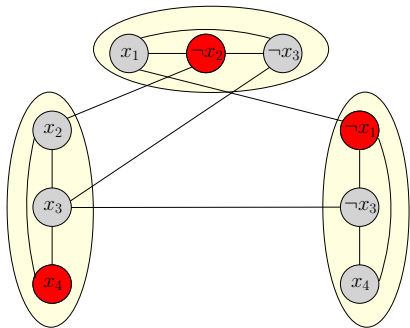
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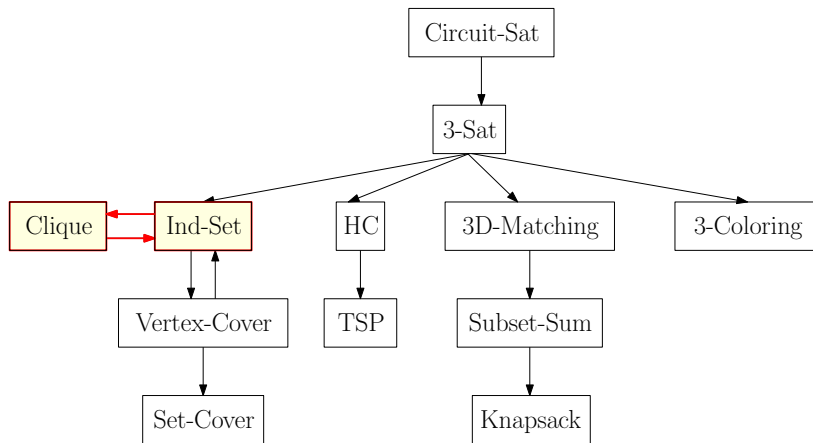


IS of Size $k \Rightarrow$ Satisfying Assignment

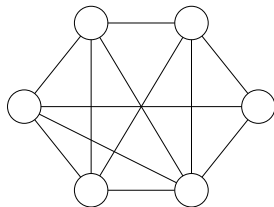
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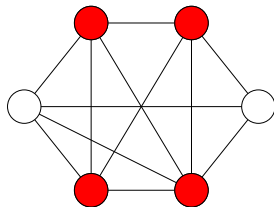
Reductions of NP-Complete Problems



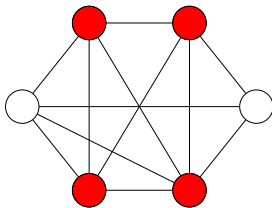
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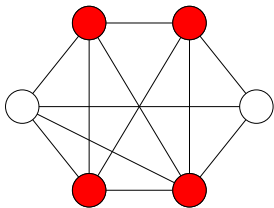


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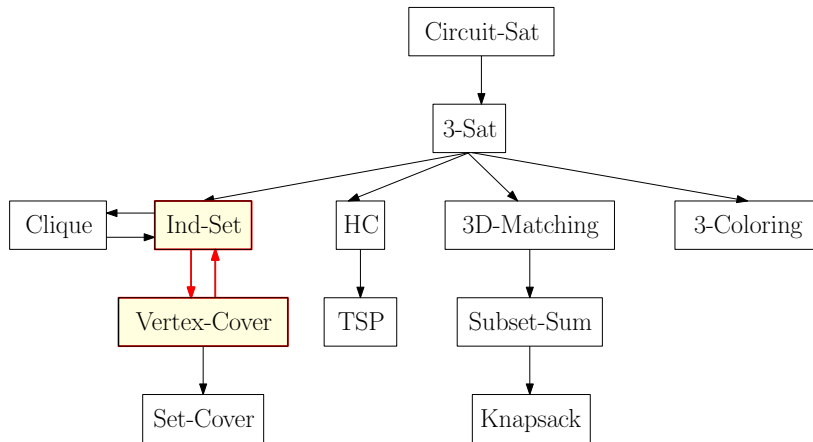
- What is the relationship between Clique and Ind-Set?

Clique \equiv_P Ind-Set

Def. Given a graph $G = (V, E)$, define $\overline{G} = (V, \overline{E})$ be the graph such that $(u, v) \in \overline{E}$ if and only if $(u, v) \notin E$.

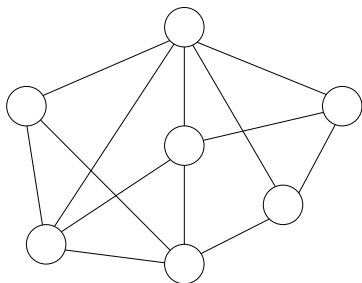
Obs. S is an independent set in G if and only if S is a clique in \overline{G} .

Reductions of NP-Complete Problems



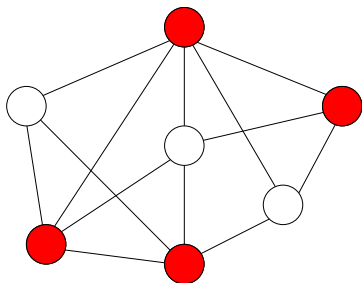
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Def. Given a graph $G = (V, E)$, a **vertex cover** of G is a subset $S \subseteq V$ such that for every $(u, v) \in E$ then $u \in S$ or $v \in S$.



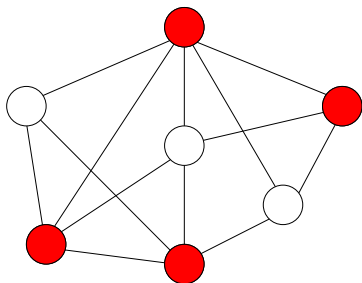
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Input: $G = (V, E)$ and integer k

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Vertex-Cover $=_P$ Ind-Set

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A: S is a vertex-cover of $G = (V, E)$ if and only if $V \setminus S$ is an independent set of G .

A Strategy of Polynomial Reduction

Recall the definition of polynomial time reductions:

Def. Given a black box algorithm A that solves a problem X , if any instance of a problem Y can be solved using a polynomial number of standard computational steps, plus a polynomial number of calls to A , then we say Y is polynomial-time reducible to X , denoted as $Y \leq_P X$.

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- That is, for a given instance s_Y for Y , we only construct one instance s_X for X

A Strategy of Polynomial Reduction

- Given an instance s_Y of problem Y , show how to construct in polynomial time an instance s_X of problem X such that:
 - s_Y is a yes-instance of $Y \Rightarrow s_X$ is a yes-instance of X
 - s_X is a yes-instance of $X \Rightarrow s_Y$ is a yes-instance of Y

Outline

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- 2 P, NP and Co-NP
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- Essentially we have no techniques for proving lower bound for running time

Dealing with NP-Hard Problems

- Faster exponential time algorithms
- Solving the problem for special cases
- Fixed parameter tractability
- Approximation algorithms

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Travelling Salesman Problem:

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- In practice: TSP Solver can solve Euclidean TSP instances with more than 100,000 vertices

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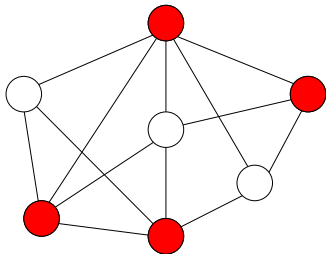
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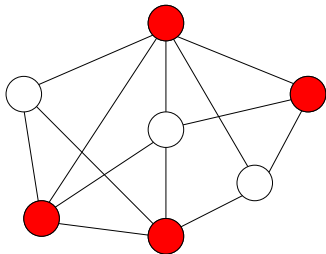
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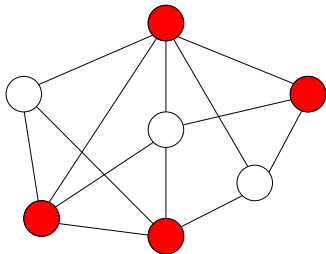
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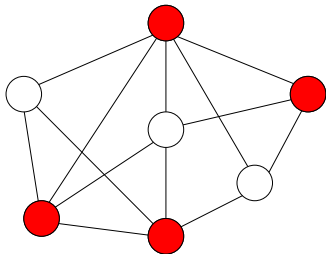
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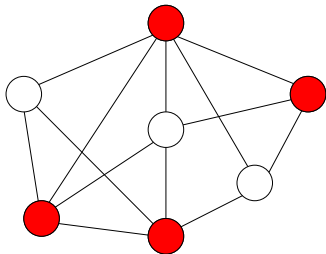
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- Vertex-Cover is fixed-parameter tractable.



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Summary

- We consider decision problems
- Inputs are encoded as $\{0, 1\}$ -strings

Def. The complexity class **P** is the set of decision problems X that can be solved in polynomial time.

- Alice has a supercomputer, fast enough to run an exponential time algorithm
- Bob has a slow computer, which can only run a polynomial-time algorithm

Def. (Informal) The complexity class **NP** is the set of problems for which Alice can convince Bob a yes instance is a yes instance

Summary

Def. B is an **efficient certifier** for a problem X if

- B is a polynomial-time algorithm that takes two input strings s and t
- there is a polynomial function p such that, $s \in X$ if and only if there is string t such that $|t| \leq p(|s|)$ and $B(s, t) = 1$.

The string t such that $B(s, t) = 1$ is called a **certificate**.

Def. The complexity class **NP** is the set of all problems for which there exists an efficient certifier.

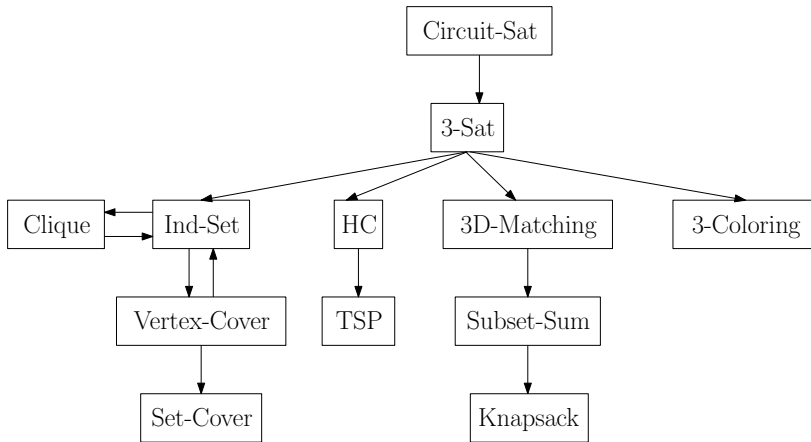
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Def. A problem X is called NP-complete if

- 1 $X \in \text{NP}$, and
 - 2 $Y \leq_P X$ for every $Y \in \text{NP}$.
- If any NP-complete problem can be solved in polynomial time, then $P = \text{NP}$
 - Unless $P = \text{NP}$, a NP-complete problem can not be solved in polynomial time

Summary



Summary

Proof of NP-Completeness for Circuit-Sat

- Fact 1: a polynomial-time algorithm can be converted to a polynomial-size circuit
 - Fact 2: for a problem in NP, there is an efficient certifier.
 - Given a problem $X \in \text{NP}$, let $B(s, t)$ be the certifier
 - Convert $B(s, t)$ to a circuit and hard-wire s to the input gates
 - s is a yes-instance if and only if the resulting circuit is satisfiable
-
- Proof of NP-Completeness for other problems by reductions