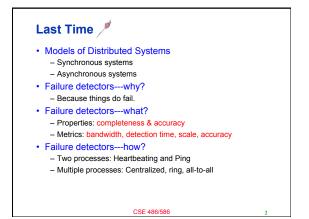
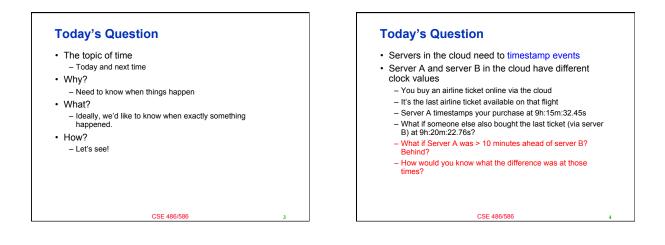


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CSE 486/586







- Some definitions: Clock Skew versus Drift
 - Clock Skew = Relative Difference in clock values of two processes
 - Clock Drift = Relative Difference in clock *frequencies (rates)* of two processes
- A non-zero clock drift will cause skew to continuously increase.
- Real-life examples
 - Ever had "make: warning: Clock skew detected. Your build may be incomplete."?
 - It's reported that in the worst case, there's 1 sec/day drift in
 - modern HW.
 - Almost all physical clocks experience this.

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Synchronizing Physical Clocks • $C_i(t)$: the reading of the software clock at process *i* when the real time is *t*. • External synchronization: For a synchronization bound *D*>0, and for source S of UTC time, $|S(t) - C_i(t)| < D$,

- for *i*=1,2,...,*N* and for all real times *t*. Clocks *C*, are accurate to within the bound *D*. Internal synchronization: For a synchronization bound D>0,
- Internal synchronization. For a synchronization bound D > 0, $|C_i(t) - C_j(t)| < D$ for *i*, *j*=1,2,...,*N* and for all real times *t*.
- Clocks C_i agree within the bound D.
- External synchronization with $D \Rightarrow$ Internal synchronization with 2D
- Internal synchronization with D ⇒ External synchronization with ??

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